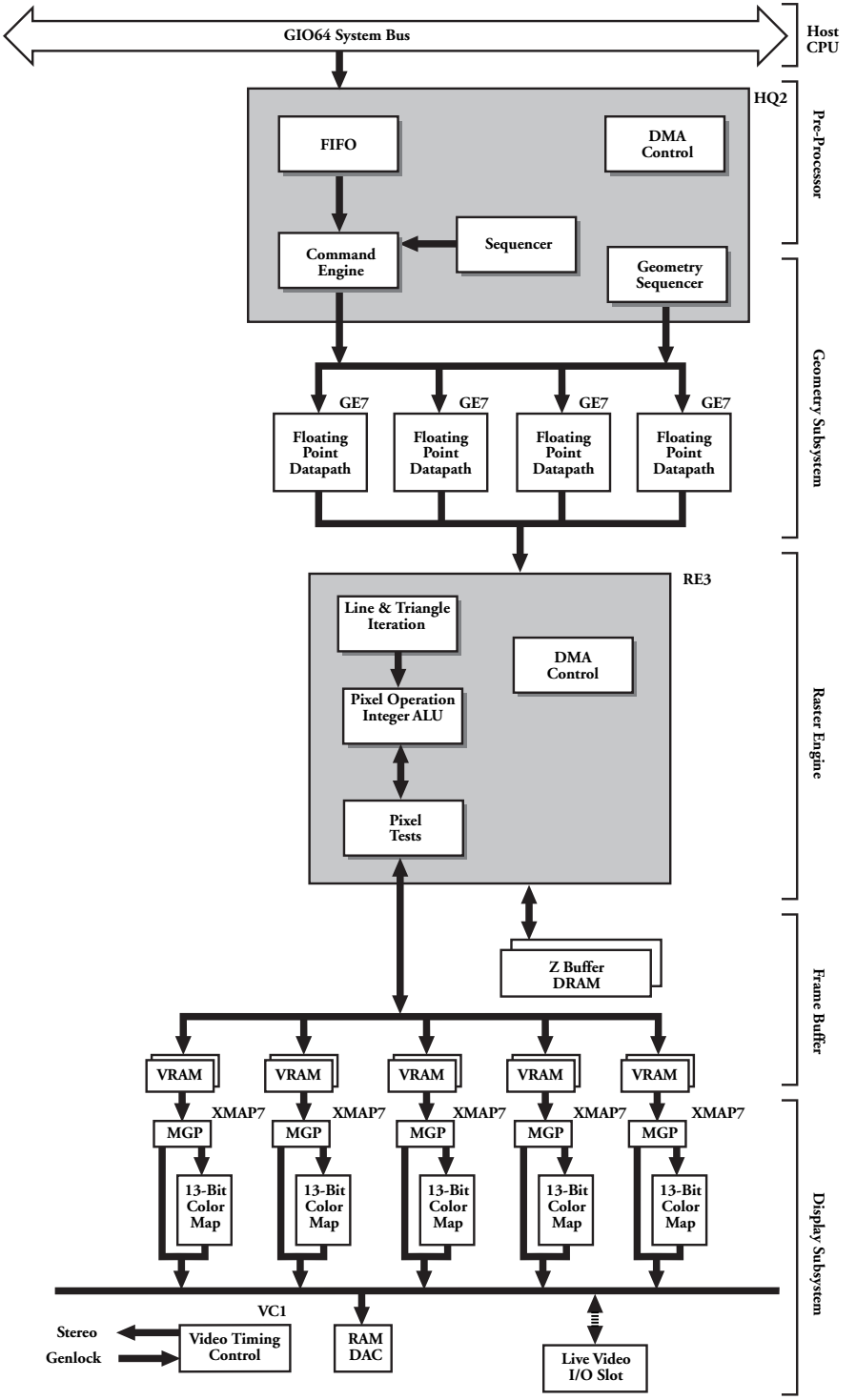
GIO64 System Bus
Host CPU
HQ2
FIFO
DMA Control
Command Engine
Sequencer
Geometry Sequencer
Pre-Processor
Geometry Subsystem
GE7
Floating Point Datapath
GE7
Floating Point Datapath
GE7
Floating Point Datapath
GE7
Floating Point Datapath
RE3
Line & Triangle Iteration
DMA Control
Pixel Operation Integer ALU
Pixel Tests
Raster Engine
Z Buffer DRAM
Frame Buffer
VRAM
VRAM
VRAM
VRAM
VRAM
XMAP7
MGP
13-Bit Color Map
XMAP7
MGP
13-Bit Color Map
XMAP7
MGP
13-Bit Color Map
XMAP7
MGP
13-Bit Color Map
XMAP7
MGP
13-Bit Color Map
Display Subsystem
VC1
Stereo
Genlock
Video Timing Control
RAM DAC
Live Video I/O Slot