

# Hardware Description

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This chapter provides a physical description of the IGX 8 and an introduction to each component's function. It describes the IGX 8 rack-mount and stand-alone enclosures, AC and DC power sources, cooling system, backplane, and system bus. The description of each card includes function, system interconnect, and faceplate indicators. A brief description of optional peripherals and third-party equipment appears at the end of the chapter. System specifications, such as protocols and standards, are listed in Appendix A.

For all matters relating to installation, troubleshooting, and repair and replacement, refer to the *IGX 8 Installation Guide*.

Other manuals that relate to IGX 8 operation are:

- *Command Reference* and *Superuser Command Reference* for descriptions of standard user commands and superuser commands, respectively
- *System Manual* for general StrataCom system and network information
- *StrataView Plus Operations* manual for information on StrataCom network management

## IGX 8 Cabinets and Components

An IGX 8 system is available in the following models:

- IGX 8 Rack-mount, an eight-slot, rack-mountable unit
- IGX 8 Stand-alone, an eight-slot unit in a free-standing cabinet

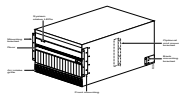
### IGX 8 Rack-Mount

The IGX 8 rack-mount system can be mounted in a standard 19" rack, such as a STRATM cabinet. This installation uses standard EIA spacing between vertical rails. Also, the vertical spacing between components, such as the card cage and fan tray, must be in the range 0.047" to 0.077" (0.119 cm to 0.196 cm). A description of the STRATM cabinet appears in the *IGX 8 Installation Manual*. The IGX 8 rack-mount cabinet contains:

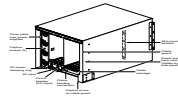
- A backplane with all purchased cards in place
- A fan tray with four fans
- An exhaust plenum
- Power connections
- An optional AC Power Tray that holds up to four power supplies

Figure 2-1 and Figure 2-2 show the rack-mount IGX 8 in front and back views, respectively.

**Figure 2-1      IGX 8 Rack-Mount, Front View**



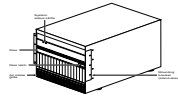
**Figure 2-2 AC-Powered IGX 8 Rack-Mount, Back View**



### IGX 8 Stand-Alone

The IGX 8 stand-alone system comes in a free-standing enclosure with a tool-operated front door latch. Four plastic feet support the IGX 8 stand-alone at its base. No mounting brackets are on the cabinet.

**Figure 2-3      Component Locations IGX 8 Stand-Alone**



The stand-alone enclosure holds all of the system modules, power modules, power supplies, and cooling fans. The IGX 8 cabinet contains one eight-slot card cage with a backplane into which front cards plug, three auxiliary slots, a fan tray with four fans, an exhaust plenum, a power entry module, and an optional AC Power Tray that holds up to four power supplies. Figure 2-3 is a front view of the IGX 8 stand-alone.

## Power Entry Back Card and Power Entry Module

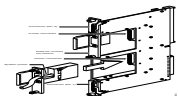
This section describes the Power Entry Back Card (PE-BC), the Power Entry Module (PEM), and the fan tray power connections to the PE-BC.

For all IGX models, the power source supplied to the unit can be either AC or DC. In a unit that uses a DC source, the power cord is attached to a Power Entry Module (PEM). The PEM provides circuit protection for the DC voltage. In a unit that uses an AC source, an optional AC power supply module is necessary for generating the DC voltages for the backplane and cooling fans.

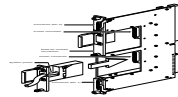
### Power Entry Back Card (PE-BC)

Each system comes with a factory-installed PE-BC. In both AC-powered and DC-powered systems, the PE-BC connects the -48 VDC primary power for the system to the backplane. It also routes power to the fan tray. Because of its shape, the PE-BC is also called the “E card.” In an AC-powered system, a cable from the supply module plugs into the PE-BC. In a DC-powered system, the PE-BC has at least one factory-installed PEM. Figure 2-4 shows the location of the PE-BC. A PE-BC slot has no number. It resides between back card slot 1 and the side of the cabinet. Figure 2-5 shows a PE-BC for an AC-powered system.

Figure 2-4      Location of the PE-BC



**Figure 2-5**      Power Entry Back Card, AC-Powered System



### Power Entry Module (PEM)

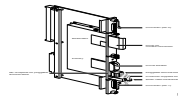
A PEM is a small, factory-installed card that plugs into a connector on the PE-BC. (The PEM plugs into the same connector as a cable from the power supply module in a system using an AC source.) A PEM takes the DC power directly from the DC power source in the building, filters it, and passes it to the PE-BC. A PEM also has a circuit breaker for turning the power on and off and a strain-relief clamp for the DC power cable. Figure 2-6 shows a PEM plugged into a PE-BC in a non-redundant system. It also shows the blank plate that covers an unused connection in a non-redundant system. Redundancy of DC sources and PEMs lets the system continue to operate in case one of the two independent DC branch circuits fails. The *IGX 8 Installation Guide* describes the DC power setup.

### Fan Power Connections

In an IGX 8, the fan tray connects to the “Fan 1” connector on the PE-BC (see Figure 2-6).



**Figure 2-6** PE-BC and DC PEM With Plug



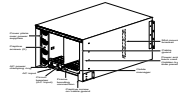
### AC Power Supplies

This section introduces the AC power system and explains the concept of redundancy.

For a node to use an AC power source, an AC power supply system is necessary. The IGX power supply housing holds up to four, 400-Watt power supplies. The AC system provides the system with –48 VDC from 100 to 240 VAC input. The AC power supply housing is between the card cage and the side wall of the node. The IGX 8 has one or, with AC redundancy, two AC power inputs.

Two types of redundancy exist: power source redundancy, in case one AC circuit fails, and AC power supply redundancy, in case a supply fails. Figure 2-7 shows two AC inputs.

**Figure 2-7**      **Power Supply Area Back View, AC System**

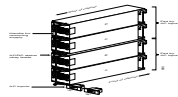


## Power Supply Requirements

The minimum requirement for an AC-powered system is one AC source and one power supply. One supply is adequate for four cards unless power supply redundancy is specified. See Table 2-1. Figure 2-8 shows the power supply slot designations and external parts of the supplies.

Figure 2-8 shows a dual-input system. Facing the back of a dual-input system, AC Input A is on the left. AC Input B is on the right. In a dual-input system, the Input A supplies power supply slots A and B. Input B supplies power to slots C and D. A single AC input system does not have these distinctions: the single input receives AC power for supplies A through C.

**Figure 2-8**      **Power Supply Slot Designations, Dual AC System**



Two types of redundancy exist in the AC-powered system. One redundancy is that of AC power inputs. A redundant AC power source from a building circuit that is independent of the other AC circuit provides backup if one AC circuit at the site fails. The other redundancy is that of the 400-Watt power supply modules. Redundancy of the 400-Watt power supplies provides a backup if a power supply fails.

In supporting single and dual AC inputs, power supply arrangements differ, as follows:

- When a system uses two AC inputs, the number of supplies that receive power from AC Input A must be the same as the number of supplies that receive power from AC Input B. See Figure 2-8.
- When a system uses one AC source, the number of supplies must be installed in a contiguous arrangement, beginning with slot A, then slot B, and so on. Because only one AC input is present, the input connection has no alphanumeric designation.

Table 2-1 shows the minimal 400-Watt unit requirements for the different IGX systems. The factors that determine the number of supplies are as follows:

- The number of AC inputs
- The number of cards (one supply can power four cards)
- Power supply redundancy for backup

In Table 2-1, the System column shows the number of AC inputs. The Number of Cards column shows the number of cards at the cutoff point for more supplies. The Supply Redundancy column indicates whether the configuration includes a redundant supply for backup. For columns A through D, an X shows that the corresponding slot in the power supply housing must have a power supply to meet the configuration requirements. The part number for a power supply is 400W-PS-AC.

**Table 2-1**      **Locations of Primary and Additional Supplies**

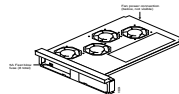
<b>System</b>	<b>Number of Cards</b>	<b>N+1 Supply Redundancy</b>	<b>A</b>	<b>B</b>	<b>C</b>	<b>D</b>
1 AC Input	4 or less	No	X			
	4 or less	Yes	X	X		
	more than 4	No	X	X		
	more than 4	Yes	X	X	X	
2 AC inputs	4 or less		X		X	
	more than 4		X	X	X	X

## Cooling System

Fans mounted in a fan tray cool the system. In an IGX 8, the fan tray resides immediately below the card cage. Each fan tray contains four fans. Figure 2-9 illustrates the fan tray.

Normally, an exhaust plenum resides at the top of each unit. Cooling air for the cards enters through grilles on the front of the fan trays and exits through the rear of the exhaust plenum

**Figure 2-9**      **Fan Tray**



## Back Card Blanks

To maintain correct air flow and reduce Radio Frequency Interference (RFI) and Electromagnetic Interference (EMI), all unused *back* card slots must be covered with the blank faceplates that StrataCom provides.

# Card Cage

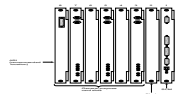
An IGX card cage has 8 active card slots (the PE-BC slot is not numbered in this scheme) on each side of the backplane. It also has space for three spare cards.

In most cases, front slots contain data processing cards (processors, service modules, etc.), and the back slots contain system interface cards. The plug-in cards in the front slots are the system unit cards. The cards in the back slots are the network trunk and user interface cards. The “Card Groups” section, later in this chapter, introduces these card and the sections that follow provide detailed information about each card.

Figure 2-10 and Figure 2-11 show typical card placements. Most cards can reside in any vacant slot. Only the NPM and SCM have specific slot assignments.

**Figure 2-10      Typical Shelf Configuration, Front View**



**Figure 2-11 Typical Shelf Configuration, IGX 8 Back View**

## Backplane (Midplane)

The IGX backplane (midplane) includes eight, 216-pin connectors. Each front card plugs into the backplane. Furthermore, except for the NPM in slot 1 and slot 2, each front card directly connects to its corresponding back card in the open area below the backplane. Thus, the upper connector on a front card plugs into the backplane, and the lower part of the front card directly plugs into a back card. The NPMs also plug into *utility bus* connectors beneath the backplane.

In addition to providing the physical connections between the cards, the backplane contains the IGX *system bus*.

### System Bus

The IGX system bus resides on the backplane. The system bus provides the following:

- The CELLBUS carries data between various service module cards. The transfer rate on the CELLBUS can reach 1 gigabit per second. This bus consists of five data lanes or highways. Four of these lanes carry data between the cards. The fifth lane provides redundancy if one of the four lanes fails.
- The power distribution bus provides –48 VDC to each of the IGX cards. Each card develops its required voltages from this –48 VDC.
- Clock distribution for system timing and card synchronization.
- The Cbus and a Dbus are serial control buses. The NPM uses these buses to monitor and control the other system unit cards and to control the flow of voice and data in the system. The NPM uses the Cbus to communicate with the application (service) cards. The NPM uses the Dbus to communicate with the adapter cards (see Adapter Cards).

As a safeguard against card driver failure or bus failure, the backplane contains redundant system buses. The backplane also contains fuses to protect it from catastrophic electrical damage. (See the chapter on repair and replacement in the *IGX 8 Installation Guide*.)

### Utility Bus

A utility bus is used to interconnect redundant processor cards in a node. In an IGX 8, the two-slot *utility bus* UBS-2 interconnects the two processor cards. It occupies the positions of the lower connectors in slots 1 and 2.

## Card Groups

The IGX currently utilizes the following functional card groups:

- Processor Card Group
- Trunk Card Group
- Channelized Voice Module Card Group
- Frame Relay Card Group
- FastPAD Card Group
- Serial Data Card Group
- Alarm Interface Card Group

This section introduces these card groups. The sections that follow contain information on the function of each card.



## Card Types

Table 2-2 lists the front cards that can be used in IGX 8 nodes. Table 2-3 lists the back cards that can be used in IGX 8 nodes. In addition to the front and back cards, the IGX 8 supports the use of Adapter Card Modules (ACMs). These cards attach to existing IPX 16/32 service module cards and perform the adaptation necessary to allow IPX 16/32 front cards to operate in an IGX 8, 16, or 32 system. Note that IPX 8-specific cards cannot be upgraded for operation in an IGX 8. Beyond this, IPX back cards can operate in an IGX system without modifications.

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**Note** Some front cards from the IGX 16/32 product line can run in the IGX 8. These are the NTM, FRM, and CVM series. This does not require adjustments or reconfiguration.

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**Table 2-2 Front Cards**

Card Acronym	Card Name
NPM	Nodal Processor Module
NTM8	Network Trunk Module
BTM	Broadband Trunk Module
CVM8-ADPCM	Channelized Voice Module, ADPCM
CVM8-T1	Channelized Voice Module, T1
CVM8-E1	Channelized Voice Module, E1
FRM8	Frame Relay Module, V.35/X.21
FRM8-2	Frame Relay Module, Port Concentrator only
FRM8-6	Frame Relay Module, w/6 chan.
FRM8-31	Frame Relay Module, w/31 chan.
FTM	FastPAD Trunk Module
HDM	High-Speed Serial Data Module
LDM	Low-Speed Serial Data Module
ARM	Alarm Relay Module

**Table 2-3      Back Cards**

<b>Card Acronym</b>	<b>Card Name</b>
SCM	System Clock Module
AIT-E3	E3 Trunk Interface Card
AIT-T3	T3 Trunk Interface Card
BC-E1	E1 Interface Card
BC-T1	T1 Interface Card
BC-Y1	Y1 Trunk Interface Card
BC-J1	J1 User (Circuit) Interface Card
BC-SR	Subrate Trunk Interface Card
FRI	Frame Relay Interface Card; V.35, T1/E1, X.21, 2-X.21
FPC	FastPAD Card; V.35, T1/E1, X.21
SDI	Synchronous Data Interface: <ul style="list-style-type: none"> <li>• RS-232C, RS-232D,</li> <li>• RS-422/449</li> <li>• X.21, V.24, and V.35</li> </ul>
LDI	Low-Speed Data Interface <ul style="list-style-type: none"> <li>• RS-232C/D, 4-port, 8-port</li> <li>• V.24, 4-Port, 8-Port</li> <li>• DDS, 4-port</li> </ul>
ARI	Alarm Relay Interface

## Common Alarms, Controls, and Indicators

FBoth front cards and back cards have faceplates with indicator LEDs and, on some front cards, push-button controls. In addition, back card faceplates have the cable connectors. In slots where no back card exists, a blank faceplate must reside to contain Electromagnetic Interference (EMI) and Radio Frequency Interference (RFI) and to ensure correct air flow.

The LED indicators are on the front and back card faceplates. The AC power supplies also have LED indicators. Each plug-in card has both a green ACTIVE LED and a red FAIL LED at the bottom of the faceplate. In general, the meaning of each LED is indicated in Table 2-4. Some other cards have additional indicators, connectors, or controls and are described in the forthcoming sections.

**Table 2-4 Common Card Status Indicators**

Indicators	Status	Meaning
FAIL	ON Steady	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
FAIL	Blinking	On an NPM in a redundant system, this indicates that the card is in an update state.
ACTIVE	ON steady	When steadily on, this indicates the card is active and carrying traffic or processing data.
ACTIVE	ON momentarily	When momentarily on, indicates the card did self-test.
BOTH	OFF	Indicates the card part of a redundant pair and is in the standby mode or not being used at all.
BOTH	ON	Indicates the card failed but remains active because no standby card is available. In the case of an HDM or LDM card, this could indicate that one or more of the data channels failed, but the others are still active.

## Processor Cards

The processor card group consists of the Nodal Processor Module (NPM) and the System Clock Module (SCM). In conjunction with the system bus, the processor card group controls communication between the cards in the node. This card group also controls communication to other nodes in the network. System timing, network control, and status reporting functions are also performed by this card group.

### Nodal Processor Module (NPM)

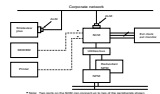
The Nodal Processor Module (NPM) is a microprocessor-based system controller that contains the software for controlling, configuring, and monitoring the IGX 8. It also maintains the alarm and event logs and controls statistics gathering. The NPM communicates with the other system plug-in cards over the control bus. Figure 2-12 illustrates the relation of the NPM to other parts of the system.

In summary, the NPM performs the following major functions:

- Runs the real-time software for controlling, configuring, diagnosing, and monitoring the IGX 8 node
- Sends configuration and control commands over the control bus to the other cards in the same node
- Receives status and alarm messages from the other cards in the node
- Generates all system bus control signals for directing the interpretation of address buses and controlling data transfers
- Communicate with other nodes in the network

The NPM communicates with all other nodes through a trunk that uses a reserved, low-speed data link of about 200 packets/sec. The NPMs communication link with other nodes carries information about new connections, topology changes, and rerouting.

**Figure 2-12 NPM in Relation to the System**



### NPM Processor and Memory Capacity

Two versions of the NPM exist. The B versions of the NPM (NPM-32B, for example) require a certain, minimal level of 8.1 software to support the +5 VDC flash memory. In addition, the two versions of the NPM have a difference in the expandability of BRAM: the NPM-B series BRAM can expand from 512 Kbytes to 2 or 4 Mbytes, and the standard NPM series can expand from 512 Kbytes to 1 Mbyte of BRAM. Both versions of the NPM can reside in the same node if the system software supports the B version. Refer to the release notes that came with this manual for the specific 8.1 version.

The NPM includes non-volatile flash EEPROM, which permits new software releases to be downloaded over the network, and battery-backup RAM (BRAM) for storing system configuration data. These memory features maintain system software and configuration data after a power failure, so the system quickly resumes operation after power returns.

The NPM or NPM-B has the following processor and minimum memory sizes:

- 68EC040 microprocessor
- 16 Mbytes of DRAM for running system software
- 4 Mbytes of flash EEPROM for downloading system software
- 512 Kbytes of BRAM for holding configuration data

For an IGX operating in a tiered network, each NPM must have 32 Mbytes of DRAM. Through factory-upgrades, memory on the NPM can expand to the following sizes:

- DRAM—64 Mbytes
- Flash EEPROM—8 Mbytes
- BRAM—1 Mbyte on the NPM and 2 Mbytes or 4 Mbytes on the NPM-B

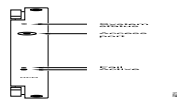
### NPM Redundancy

An IGX 8 has one NPM in a non-redundant system or two NPMs in a redundant system. In a non-redundant system, an NPM resides in either front slot 1 or front slot 2. For a redundant system, NPMs reside in slots 1 and 2. The NPM plugs into the system bus backplane. A utility bus in the backplane connects the NPMs in a redundant system.

### NPM Faceplate and LEDs

The faceplate of the NPM has a green ACTIVE LED and a red FAIL LED. See Figure 2-13. The NPM monitors its own activity and, if a failure is detected, the FAIL LED is lit. If the node has redundant NPMs, the on-line NPM is indicated by the lit ACTIVE LED, while the standby NPM has no lit indicators. In addition to the status LEDs on the NPM faceplate, information on any NPM can be displayed at a terminal by executing the **dspecd** command.

**Figure 2-13     NPM Faceplate**



## System Clock Module (SCM)

The System Clock Module (SCM) card provides a centralized clock generation function for the IGX. It generates the system clock and trunk synchronizing clocks. The SCM phase-locks the internal IGX timing to the selected clock source for network synchronization. Each IGX node must have an SCM.

The SCM plugs directly into the NPM card in slot 1. The NPM and SCM card set are the backbone of the IGX system. The NPM controls and monitors the SCM control buses. A single SCM can support redundant NPMs.

In addition, the SCM provides:

- Provides two, low-speed communications paths for connecting a control terminal, printer, and/or modem(s)
- Provides a single, high-speed LAN communications path for connecting the Network Management System (NMS)

The SCM circuits include of the following:

- Clock detection, alarm status, and control from the NPM
- Phase lock loops
- External clock input
- Clock, power supply, temperature, and fan monitoring
- Bus expansion
- Reset circuitry
- Control terminal, printer/modem, and LAN AUI ports
- 2 RS-232 serial port interfaces
- 1 Ethernet port interface

The two serial RS-232 ports provide connection to network management terminals and modems for remote access to the node. In conjunction with the SCM, the NPM also supports a high-speed Ethernet LAN port for faster system statistics transfer between the node and a StrataView Plus NMS workstation. This port conforms to the requirements of IEEE standard 802.3 for Ethernet.

The SCM has duplicates of the internal clock circuitry and its associated phase lock loops and NPM-related control circuitry. One operates off the System A Bus, and the other operates off the System B Bus. Both circuits are independent and are monitored separately to provide complete backup if a circuit fails (**FAIL** lamp comes on). However, since both the System A bus and System B bus clock circuits exist on a single card, removing the SCM disrupts system operation. The lower priority SCM circuits are not duplicated. The lower priority circuits are the external clock input, control and auxiliary ports, and monitoring circuits for power supplies, cabinet temperature, and fans. A failure in a lower priority circuit does not cause a system failure, but the SCM reports the problem.

The Ext Clock connector on the faceplate of the SCM provides an interface for an external source for a high-stability clock. This clock is configurable as the primary, secondary, or tertiary clock. This input is 1.544 MHz for T1 systems and 2.048 MHz for CEPT systems. In addition, one of the trunk or circuit line inputs may also serve as a source of timing for the node. If no clock source is selected, the clock source is the internal IGX clock.

Refer to Figure 2-14 and Table 2-7 for details on the SCM faceplate. When correlating the descriptions in the table with the callouts in the figure, read from the top down.

In addition to the clock functions, the SCM provides a pair of low-speed, serial communications ports. The CONTROL TERMINAL port is a bi-directional port for connecting the IGX 8 to a local network control terminal or to a modem for remote terminal connection. The AUXILIARY PORT connects to a maintenance log printer, an additional dumb terminal, an alarm message collector, external device window, or an auto-dial modem for automatic reporting of local IGX alarm conditions. This modem can be programmed to dial StrataCom ISC for assistance when a network alarm occurs.

Figure 2-14     SCM Faceplate

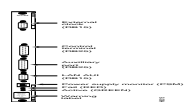


Table 2-5     SCM Faceplate Connectors and Indicators

Connector	Function
EXTERNAL CLOCK	DB15 connector for connecting an external source for the system clock.
CONTROL TERMINAL	A DB25 connector for a VT100 or equivalent terminal for a basic network management terminal. It can also be connected to a dial-in modem for ISC or other network management dial-up access. This is a bidirectional RS232 communications port.
AUXILIARY PORT	A DB25 connector for a system printer. Can also be used to connect to an out-dial modem or automatic reporting of alarms in the event the node becomes isolated. This is a bidirectional RS232 port.
LAN AUI	Ethernet LAN connection for multiple, local StrataView Plus NMSs. This port uses a DB-15 connector called the Attachment Unit Interface (AUI) connector. This name reflects the industry standard term for a LAN interface. See Table 2-6 for the AUI pin assignments.
PSM	Power supply monitor, cable supplied by customer.
FAIL	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
ACTIVE	Indicates the card is in service with active circuits.



Table 2-6 lists the pin assignments for the LAN connector.

**Table 2-6      AUI Connector Pin Assignments (DB 15-connector)**

Pin	Name	Pin #	Name
1	Shield	—	—
2	Collision Presence +	9	Collision Presence—
3	XMT +	10	XMT—
4	Reserved	11	Reserved
5	RCV +	12	RCV—
6	Power return	13	Power (+12V)
7	Reserved	14	Reserved
8	Reserved	15	Reserved

## Adapter Cards

IPX service/interface cards can be upgraded for use in an IGX 8. This allows the IGX to provide all the services of the IPX with cards of proven efficiency, functionality, and reliability. The upgrade is available only as a factory upgrade. The factory upgrade consists of an adding one of three possible Adapter Card Modules (ACM) and possible firmware or hardware modifications. Due to the complexity of the ACM, field upgrades of IPX cards are not possible.

Connecting IPX front cards to their corresponding back cards on the IPX requires the use of a utility or local bus. On upgraded IPX cards (IGX cards), the local or utility bus is not necessary.

The following IPX cards can be adapted for use in the IGX 8:

- NTC
- AIT
- CDP
- FRP
- FTC
- SDP
- LDP
- ARC

## Trunk Interface Cards

The IGX supports two types of trunk cards:

- The Network Trunk Module (NTM8 front card)
- The Broadband Trunk Module (BTM front card)

The NTM8 works with the following back cards:

- T1 Interface card (BC-T1)
- E1 Interface card (BC-E1)
- Subrate Interface card (BC-SR)
- Y1 (Japan) Interface card (BC-Y1)

The BTM works with the following back cards:

- AIT-T3 Interface card
- AIT-E3 Interface card

## Network Trunk Module (NTM8)

The Network Trunk Module for the IGX 8 (NTM8) coordinates the transmission of FastPackets across a trunk line to another node. Note that the NTM and NTM8 are functionally the same and that the NTM can operate in an IGX 8, but the NTM 8 cannot operate in an IGX 16/32.

NTM8 functions include the following:

- Receives FastPackets from the CELLBUS and queues them in separate queues for transmission on the selected trunk
- Arbitrates access to the trunk for the traffic type
- Monitors the age of each timestamped FastPacket, updates the timestamp for FastPackets at intermediate nodes, and discards FastPackets that exceed age limit
- Receives and checks FastPackets from the trunk and queues them for transmission to the System Bus
- Provides packet alignment based on the CRC in the FastPacket header
- Extracts clocking from the trunk that can be used as a clock source on the node or as a clock path
- Collects trunk usage statistics

An NTM8 can occupy any available front slot in the range 3 to 8. The choice of back card depends on the trunk interface type. All the service card slots may hold NTM8s.

For fractional T1 trunk lines, the NTM8 and BC-T1 card set can provide the interface. Fractional trunk interfaces use a group of 64-Kbps channels, which constitute a partial T1 trunk. For example, a 512-Kbps fractional T1 trunk might use every third channel from 1 through 24. Fractional trunks use the basic trunk frequency (such as 1.544 Mbps for T1) for the clock rate. The network operator makes the channel assignments.

Fractional E1 is the same as fractional T1 except that the channels are 1 to 15 and 17 to 31 (0 and 16 reserved) and the clock rate is 2.048 Mbps).

The NTM8 supports subrate trunks if a BC-SR back card and appropriate local bus are present. Subrate trunks interface to the digital transmission facility at trunk rates in the range 256 Kbps through 2.048 Mbps. Three interface connections are provided. These are RS449, X.21, and V.35.

## NTM8 and Back Card Redundancy

The NTM8 can be configured for 1:1 redundancy by using a second, identical, card group and a Y-cable for connection to the trunk. All of the back cards support redundancy.

## NTM8 Status

The faceplate of the NTM8 card has four LEDs. An ACTIVE LED indicates the card is active and functioning normally. An NTM8 card failure lights up the FAIL LED. The other two LEDs are a summary alarm for the back card conditions. A yellow MINOR LED indicates non-service interrupting faults or when statistical errors have exceeded a preset threshold. A red MAJOR LED indicates service-affecting failures.

Alarms/line conditions monitored by the NTM8 include those in the following list. To view packet line errors, use the **dspplnerrs** command. To see what packet line errors are configured for display, use **dspplnstatcnf**.

- E1 CRC4 errors
- T1 Bipolar violations
- Near end and far end frame alarms
- Alarm Information Signal (AIS)
- Loss of signal
- Line signal frame sync losses
- Packet out of frame

## T1 Interface Card (BC-T1)

The T1 Trunk Interface Card (BC-T1) card terminates a single 1.544 Mbps T1 trunk line on the NTM8. The BC-T1 can reside in any rear slot 3-8 of the IGX 8. The BC-T1 connects directly to the NTM8.

The BC-T1 provides the following:

- Trunk line interfaces to T1 trunks at 1.544 Mbps
- Software selectable AMI or B8ZS (bipolar 8 zero suppress) line code
- Software selectable D4 or ESF (Extended Superframe) framing format
- Configuration as either full or fractional T1 service
- Extraction of receive timing from the input signal for use as the node timing
- Software selectable line buildout for cable lengths up to 655 feet
- Passes line event information to the front card

B8ZS supports clear channel operation because B8ZS eliminates the possibility of a long string of zeros. B8ZS is preferable whenever available, especially on trunks.

The BC-T1 supports two clock modes. These are *normal clocking* and *loop timing*. The system operator selects the mode through software control. Normal clocking uses the receive clock from the network for incoming data and supplies the transmit clock for outgoing data. This clock can be used to synchronize the node.

Loop timing uses the receive clock from the network for the incoming data and turns the receive clock around for timing the transmit data.

BC-T1 Faceplate Description

Figure 2-15 and Table 2-7 provide information on the faceplate of the BC-T1. When correlating the descriptions in the table with the callouts in the figure, read from the top down. The standard port connector is a female DB15.

**Figure 2-15      BC-T1 Faceplate**



**Table 2-7 BC-T1 Connections and Status LEDs**

Connector/Indicator	Function
T1 INPUT/OUTPUT	Female DB15 connector for T1 line.
<b>LOS</b> light (red)	Indicates loss of signal at the local end.
<b>Red Alarm</b> light (red)	Indicates loss of local E1 frame alignment or packet alignment (NTM8).
<b>Yellow Alarm</b> light (yellow)	Indicates loss of frame alignment at remote end or loss of packet alignment (NTM8).
<b>AIS</b> light (green)	Indicates the presence of all ones on the line.
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
<b>ACTIVE:</b> light (green)	Indicates the card is in service with active circuits.

## E1 Interface Back Card (BC-E1)

The E1 Trunk Interface Card (BC-E1) provides an E1 trunk interface for the Network Trunk Module 8 (NTM8). The BC-E1 connects directly to the NTM8 and can reside in back slots 3 to 8 in an IGX 8. The BC-E1 provides the following:

- Interfaces to CEPT E1 lines (CCITT G.703 specification)
- Support for both Channel Associated Signalling and Common Channel Signalling
- Support for unframed 32 channel (2.048 Mbps), framed 31 (1.984 Mbps), or 30 (1.920 Mbps) channel operation, or any  $n \times 64$  Kbps. rate down to 256 Kbps.
- Configuration of either full or fractional E1 lines
- CRC-4 error checking
- Support for HDB3 (clear channel operation on E1 lines) or AMI
- Passes E1 line events (Frame loss, Loss of signal, BPV, frame errors, CRC errors, CRC synchronization loss, etc.) to the front card
- Detection of loss of packet sync when used with the NTM
- 120-Ohm balanced or 75-Ohm balanced or unbalanced physical interfaces

The BC-E1 supports two clock modes. These are *normal clocking* and *loop timing*. The system operator selects the mode through software control. Normal clocking uses the receive clock from the network for incoming data and supplies the transmit clock for outgoing data. This clock can be used to synchronize the node.

Loop timing uses the receive clock from the network for the incoming data and turns the receive clock around for timing the transmit data.

Statistics are kept on most line errors and fault conditions, including the following:

- Loss of signal
- Frame sync loss
- Multi-frame sync loss
- CRC errors
- Frame slips
- Bipolar violations
- Frame bit errors
- AIS, all-1's in channel 16 (CAS mode)

Figure 2-16 shows and Table 2-8 lists status LEDs and connections on the BC-E1 faceplate. When correlating the table and figure items, read from the top down.

**Figure 2-16 BC-E1 Faceplate**



**Table 2-8 BC-E1 Connections and Status LEDs**

Connector/Indicator	Function
RX-TX	Female DB15 connector for XMT and RCV E1.
RX	BNC connector for receive E1 line.
TX	BNC connector for transmit E1 line.
<b>LOS</b> light (red)	Indicates loss of signal at the local end.
<b>Red Alarm</b> light (red)	Indicates loss of local E1 frame alignment. On an NTM8, indicates loss of packet alignment.
<b>Yellow Alarm</b> light (yellow)	Indicates loss of frame alignment at remote end. On an NTM8, indicates loss of packet alignment.
<b>AIS</b> light (green)	Indicates the presence of all ones on the line.
<b>MFRA</b> light (red)	Indicates loss of multiframe alignment (E1 only).
<b>MFRY</b> light (yellow)	Indicates loss of multiframe at remote end (E1 only).
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, StrataCom call the ISC.
<b>ACTIVE:</b> light (green)	Indicates the card is in service with active circuits.

## Subrate Interface Card (BC-SR)

The Subrate Trunk Interface Card (BC-SR) terminates subrate trunks on the NTM8. A subrate trunk uses only a portion of the E1 or T1 bandwidth. It is used primarily for tail circuits or where little traffic exists. A subrate trunk facility interface is a DCE interface, and the subrate channel acts like a synchronous data channel. Therefore, the IGX BC-SR always has a DTE configuration. Only leased lines are supported. Dial-up lines are not supported. Subrate trunks cannot pass clock signals between nodes.

The BC-SR provides the following:

- Trunk line interfaces to subrate trunks
- Trunk rates of: 256 Kbps, 384 Kbps, 512 Kbps, 768 Kbps, 1.024 Mbps, 1.536 Mbps, and 1.920 Mbps., 2.048 Mbps.
- V.11/X.21, V.35, and MIL188/RS-449 interfaces
- Synchronizes the trunk clocking with looped clock option (not applicable to X.21)
- A limited set of EIA control leads that are monitored by the system

Table 2-17 and Table 2-9 provide information on the BC-SR faceplate. When correlating the items in the figure and table, read from the top down.

Figure 2-17 BC-SR Faceplate



Table 2-9 BC-SR Connections and Status LEDs

Connector/Indicator	Function
RS-449/MIL 188 data connector	DB37 female connector
X.21 data connector	DB15 female connector.
V.35 data connector	34-pin female MRAC connector
LOS light (red)	Indicates loss of signal at the local end.
Bad CLK light (red)	Indicates loss of clock or clock out of range
Yellow Alarm light (yellow)	Indicates loss of packet alignment (NTM8) or frame alignment at remote end
DSR light (green)	Indicates the DSR lead is high (ON)
DTR light (green)	Indicates the DTR lead is high (ON)
RXD light (green)	Indicates the receive data line shows activity
TXD light (green)	Indicates the transmit data line shows activity.
FAIL light (red)	Indicates an error occurred. Resetting the card with the <b>reseted f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
ACTIVE: light (green)	Indicates that the card is in service and that active circuits are present.



Table 2-10 lists the data signals and EIA leads supported by the substrate interface.

**Table 2-10 Data and Control Leads Supported with BC-SR**

Transmit			Receive		
Lead	Name	Interface	Lead	Name	Interface
TX	Transmit data	All	RX	Receive data	All
RTS	Request to Send	188, V.35	CTS	Clear to Send	188, V.35
DTR/C	Data Terminal Ready	All	DSR/I	Data Set Ready	All
LL	Local Loop	188	DCD	Data carrier select	188, V.35
RL	Remote Loop	188	RI/IC	Ring Incoming Call	188, V.35
IS	Terminal In Service	188	TM	Test mode	188, V.35
SS	Select standby	188, V.35	SB	Standby indicator	188
SF	Sig rate select	188	SI	Signalling rate	188

## Y1 Interface Back Card (BC-Y1)

The BC-Y1 back card provides a Japanese Y1 trunk interface for an NTM8. The BC-Y1 can reside in any slot 2-8 of the IGX 8. The BC-Y1 provides:

- An interface to Japanese “Y” Trunk (Y1) lines
- Support for Y1 Trunk formatted signalling
- Support for 24 channel, 1.544 Mbps operation
- Support for fractional rates
- Coded Mark Inversion (CMI) line coding
- Statistics for Y1 line events (loss of framing, loss of signal, framing errors, etc.)
- Support for local and remote loopback at the Y1 interface as well as the local bus interface for fault isolation.

The BC-Y1 supports two clock modes. These are *normal clocking* and *loop timing*. The system operator selects the mode through software control. Normal clocking uses the receive clock from the network for incoming data and supplies the transmit clock for outgoing data. This clock can be used to synchronize the node.

Loop timing uses the receive clock from the network for the incoming data and turns the receive clock around for timing the transmit data.

Figure 2-18 and Table 2-11 provide descriptions of the BC-Y1 status LEDs and connections on the faceplate. When correlating the items in the figure and table, read from the top down.

Figure 2-18 BC-Y1 Faceplate



Table 2-11 BC-Y1 Connections and Status LEDs

Connector/Indicator	Function
Y1Trunk INPUT/OUTPUT	DB15 connector for Y1 Trunk
Line in	Y1 trunk input line
Line out	Y1 trunk output line
RX MON	BNC test connector for monitoring receive Y1 line
TX MON	BNC test connector for monitoring transmit Y1 line
LOS light (red)	Indicates loss of signal at the local end.
Red Alarm light (red)	Indicates loss of local frame alignment.
Yellow Alarm light (yellow)	Indicates loss of frame alignment at the remote end.
AIS light (green)	Indicates the presence of all ones on the line.
FAIL light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
ACTIVE light (green)	Indicates that the card is in service and that active circuits are present.

## Broadband Trunk Module (BTM)

The BTM front card provides an Asynchronous Transfer Mode (ATM) trunk interface so that the IGX can use the standard ATM cell relay protocol over T3/E3 trunks. The trunk rate can be as low as one T1/E1 rate. This supports migration up to T3/E3 rates in T1/E1 increments with minimal downtime for traffic rerouting. The BTM card set consists of the BTM front card and either an AIT-T3 or an AIT-E3 back card.

The card set works in the following arrangements:

- IGX-to-IGX (BTM-to-BTM)
- IGX-to-BPX communication over broadband ATM trunks (BNI)
- IGX-to-IPX (BTM on the IGX to AIT on the IPX)
- IGX frame relay connections terminating on an AXIS (complex gateway)

Descriptions of broadband trunk implementation, ATM protocol, and cell and header formats appear in the *System Manual*.

## Operating Modes

The BTM/AIT card set can operate in either *simple gateway* or *complex gateway* mode. Complex gateway supports *network interworking* and allows all StrataCom frame relay interfaces to communicate. It requires release 8.1 or higher system software. The application of complex gateway is in *tiered networks*. For a description of tiered networks, refer to the *System Manual*.

Simple gateway supports frame relay connections that terminate on either an IPX or IGX in a flat network. Simple gateway does not support frame relay connections between the AXIS FRSM and the IGX FRM/FRI card set and does not require 8.1 system software.

## Maintenance and Troubleshooting

No maintenance is required on BTM cards. If a card has either a solid or a confirmed intermittent failure, replace it. The only faceplate indicators are the ACTIVE and FAIL LEDs. For purposes of troubleshooting, the BTM/AIT card set is considered a trunk. The **tstcon** command does not work on a BTM because the card cannot be isolated from the IGX or the other, connected BTM.

A trunk loopback test is run when the BTM detects an integrated alarm. The loopback test verifies if the line or the card is faulty. A loopback test “pass” means the line is faulty and a line alarm is indicated. A loopback test “fail” means the card is faulty. In the case of a faulty card, a switch to a Y-Cable equipped redundant card occurs if available.

## BTM Back Card Description (AIT-T3 and AIT-E3)

The back card for the BTM is available in two versions. One version supports a single ATM T3 port, and the other supports a single ATM E3 port. These back cards are the AIT-T3 and AIT-E3, respectively. The back card provides the interface to the trunk and performs all necessary CRC generation and checking. The trunk port consists of one BNC connector for transmit and one BNC connector for receive. The back card faceplate has six LED indicators. They indicate the status of the port and various alarm conditions (see Figure 2-19 and Table 2-12 for details).

LED Indicators and Alarms

The faceplate of the BTM has four LEDs. The ACTIVE LED indicates the card is active and functioning normally. A BTM card failure triggers the FAIL LED. The other two LEDs are a summary alarm for the AIT back card conditions. A yellow MINOR LED indicates either that a non service-interrupting error has occurred or error statistics have exceeded a preset threshold. A red MAJOR LED indicates a service-affecting failure.

The AIT -T3 back card has two BNC connectors and six LED indicators, as Figure 2-19 shows. Table 2-12 lists these faceplate items. When correlating the items in the figure and table, read from the top down.

**Figure 2-19      AIT-T3 Back Card Faceplate**



**Table 2-12 AIT-T3 Connections and Indicators**

Connector/Indicator	Function
Transmit Jack	BNC connector for transmit data
Receive Jack	BNC connector for receive data
<b>LOS</b> light (red)	Indicates loss of signal at the local end.
<b>Red Alarm</b> light (red)	Indicates loss of local T3 or E3 frame alignment, or cell alignment.
<b>Yellow Alarm</b> light (yellow)	Indicates loss of frame alignment at remote end, or cell alignment.
<b>AIS</b> light (green)	Indicates the presence of all ones on the line.
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the resetcd f command is suggested first. If the LED comes on again, call the StrataCom ISC.
<b>ACTIVE:</b> light (green)	Indicates that the card is in service and that active circuits are present.

### Y-Cable Redundancy

The BTM card set supports Y-cable redundancy on ATM trunks in IGX to IGX, IGX to IPX and IGX to BPX applications. Y-Cable Redundancy is an existing IGX feature that may be applied to ATM trunks. (In the *Command Reference*, see commands **addyred**, **delyred**, **dspyred**, and **ptyred**.)

Y-Cable Redundancy requires that both cards are upped (**uptrk**) and added (**addtrk**) before assigning redundancy.

### Y-Cable Redundancy Switching

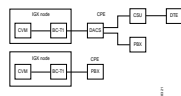
The BTM card performs a clock test on the input line source. If either the clock or card fails, a switch occurs to a Y-cabled redundant BTM trunk card set if available. If the switch occurs, the primary ATM trunk card acquires failed status, and the red FAIL indicator is turned on. If Y-cable redundancy is not available, the ATM trunk switches to another clock source and marks the line as a failed clock source.

## Channelized Voice Module (CVM8)

The Channelized Voice Module (CVM8) is a dual-purpose front card that provides a method of terminating a T1, E1, or J1 circuit line (a communication line to CPE). The circuit line may carry either voice traffic alone, a combination of voice and low-speed or high-speed data, or just data. See Figure 2-20. The channel default is voice mode until the interface type is reconfigured. The CVM8 data capabilities include DS0-A, 56/64 xN data rates. The CVM8 voice capabilities include A-law to  $\mu$ -Law conversion and voice compression. These are configurable for each channel.

The CVM8 can be programmed for either 24-channel mode (T1) or 30-channel mode (E1). The circuits on a CVM8 hop are assigned on a per-timeslot basis within a T1 or E1 frame.

The CVM8 can reside in any front slot 3-8 in an IGX 8. The CVM8 operates with either a BC-T1, BC-E1, or BC-J1 back card. The back cards plug directly into the P2 connector on the front card. For details on the back cards, refer to “T1 Interface Card (BC-T1),” “E1 Interface Back Card (BC-E1),” and “BC-J1 Description” sections, which describe the BC-T1, BC-E1, or BC-J1, respectively.

**Figure 2-20 CVM8 Application Diagram**

## CVM8 Features

The features of the CVM8 front card include:

- Packet assembly/disassembly (PAD) for voice or data connections on one card
- Sub-rate, standard rate, and super-rate data connections
- Software configurable T1, E1, or J1 ports, using appropriate back card
- Standard rate voice connections with or without compression
- Up to 4:1 voice compression using integral Voice Activity Detection VAD and ADPCM
- Integral, per-channel, echo cancelling using optional piggyback cards
- A-law or  $\mu$ -law voice encoding on a per channel basis
- Programmable voice circuit gain of between  $-8$  and  $+6$  db
- Many domestic and international signalling types supported
- Per channel tone detection to disable compression for modem or FAX circuits
- Complete self-test of all on-board circuits including echo cancellers
- Local and remote loopbacks for port and circuit testing
- Flexible signalling bit conditioning upon circuit alarm
- Nominal transmission loss at 1 KHz is zero dB

## Types of CVM8 Circuits

Table 2-13 introduces the types of CVM8 circuit operation. (Sections that follow expand upon these subjects.) Two types of 64 Kbps clear channel operation are listed. The “p” indicates uncompressed PCM voice. The “t” indicates 64 Kbps clear channel data. Beyond these, different bit rates (with compression) and voice activity detection (voice connections only) are shown. A “c” or an “a” that precedes a numerical value indicates compression. The “c” indicates compression with VAD, and the “a” indicates compression without VAD. Therefore, “c” does not apply to data connections, and “a” can apply to either voice or data. The numerical value following the “a” or “c” is the bit rate. The table also defines the significance of the other characters that may follow the bit rate.

**Table 2-13      Types of CVM8 Circuits**

Type	Description
p	A p-type connection carries 64 Kbps PCM voice and supports A-law or $\mu$ -law encoding and conversion, level adjustment (gain/loss), and signalling.
t	VF t-type connections carry 64 Kbps clear channel data traffics.
cxx	When using the CVM8 card for voice circuits, the level of ADPCM compression must be specified by the <i>x</i> parameter and can be 32, 24, or 16 for 2:1, 3:1, or 4:1 compression. For example "c16" indicates full 4:1 compression with VAD.
a16z c16z	The c16z and a16z use a 4-level 16 Kbps ADPCM to ensure ones-density but at a decrease in voice quality. The "z" acts as a route-avoid specification whose effect is to avoid lines with ZCS (zero code suppression).
a32d c32d	The a32d and c32d circuit types pertain to Enhanced Instafax. These types support high speed circuits but stay at 32 Kbps when a high-speed circuit is detected and thus support compression that would otherwise be unavailable. Unless a32d or c32d is selected, the circuit switches to 64 Kbps when a high-speed modem or FAX is detected on a voice circuit. This selection is made using the <b>cnfvchparm</b> command. The selection of a32d or c32d depends on the performance of the modem/FAX using the circuit and should be selected for error-free operation.
a32 a24 a16	Uses ADPCM only. Can select 32 Kbps, 24 Kbps, or 16 Kbps compression. Compressed code avoids all zeros and can be used on lines with no other zero code suppression techniques. Modified 16 Kbps compression.
c32 c24 c16	Uses both ADPCM and VAD. Can select 32 Kbps, 24 Kbps, or 16 Kbps ADPCM compression. Compressed code avoids all zeros and can be used on lines with no other zero code suppression techniques. 16 Kbps compression is non-standard.

Standard-rate (64 Kbps) voice connections originate and terminate on CVM8 T1 or E1 lines. The encoding type (A-law or  $\mu$ -law), voice compression, and echo cancelling is configured on a per-channel basis according to the circuit requirements. The voice coding rate can be selected for 64 Kbps (no compression), 32 Kbps (2:1), 24 Kbps (2.67:1), or 16 Kbps (4:1). These compression ratios approximately double when the internal Voice Activity Detection (VAD) feature is also enabled.

The CVM8 extracts information from the signalling bits in the E1 or T1 frame and generates signalling packets when a signalling bit changes. These packets go from the local CVM8 to the far-end CVM8. DPNSS and ISDN signalling are supported through a clear channel (transparent mode). The signalling choices for the different configurations are:

- T1 uses ABCD or ABAB signalling bits with ESF line framing.
- T1 uses AB signalling bits with D4 framing.
- T1 can use CCS in timeslot 24 on applicable PBXs if turning off CAS is preferable.
- E1 uses either CCS or, when CAS is used, ABCD signalling bits.

Up to 23 voice interface types, such as 2-W E&M, FXO/FXS, DPO/DPS may be selected from a template to condition the VF signalling. Specifying any custom signal conditioning is also possible. Voice channel signalling can be programmed for any of the following:

- Robbed bit (either D4 or ESF frame format)—for T1 lines
- Channel Associated Signalling (CAS)—for E1 lines
- Transparent CCS (ISDN & DPNSS)—for T1 or E1 lines
- E&M to DC5A and DC5A to E&M conversion—for international applications

Local and remote loopbacks are available for intranode, CVM8-terminated connections. Local loopback exists at the system bus interface and returns data and supervision back towards the local facility to test the local CVM8 card set and customer connection. Remote loopback connects to the CVM8 at the far end of the circuit and checks both directions of transmission and much of the remote CVM8.

## CVM8 Faceplate Description

The CVM8 faceplate has four LED indicators: ACTIVE, FAIL, MAJOR, and MINOR (see Table 2-14). The faceplate also shows the type of CVM8 card by way of the CVM8 label.

**Table 2-14 CVM8 Faceplate Indicators**

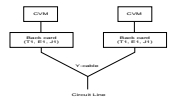
Indicator	Meaning
ACTIVE (green)	On indicates card is active and carrying traffic.
FAIL (red)	On indicates self-test has detected a card failure in the main card or optional integrated echo canceller board.
MAJOR (red)	A line failure has been detected locally on the receive side.
MINOR (yellow)	A line failure has been detected at the remote end.
Label CVM8	Standard CVM8
CVM8 ADPCM T1	T1 with echo cancelling module
CVM8 ADPCM E1	E1 with echo cancelling module

The CVM8 monitors and reports statistics on the following input line conditions:

- Loss of signal
- Frame sync loss
- Multi-frame sync loss
- CRC errors
- Frame bit errors
- Frame slips
- Bipolar violations
- Frame bit errors
- AIS, all-1's in channel 16 (CAS mode)
- Remote (yellow) alarm

The CVM8, BC-T1, BC-E1, or BC-J1 support 1:1 redundancy by use of a second, identical, card set and a Y-cable connection to the circuit line. See Figure 2-21 for an illustration.



**Figure 2-21 Y Cable Redundancy**

## CVM8 Voice Circuits

The CVM8 at each end of a connection is programmable for the two different voice encoding schemes. These encoding schemes are A-law and  $\mu$ -law, and they operate at E1 and T1 rates, respectively. Thus, the CVM8 supports international gateway applications. The CVM8 supports high-speed modem and FAX circuits. The CVM8 provides transparent, 64 Kbps. transport for CCS signalling. It also supports CAS signalling by transporting signalling transitions across the network with negligible distortion. The CVM8 can set, invert, and clear AB or ABAB bits (T1) or ABCD bits (E1) to accommodate some signalling conversions.

Echo cancellers are almost always required on voice circuits to compensate for the delays introduced with the various voice compression techniques used by the CVM8. A built-in feature of the CVM8 is the on-board Integrated Echo Canceller (IEC). This eliminates having to provide this necessary feature with external equipment. Two models of the IEC are available: a 24-channel (T1) and a 31-channel (E1).

## CVM8 Data Circuits

The CVM8 can provide data connections to the network. Data connections that originate on a CVM8 can terminate on either a CVM8, HDM, or LDM card on another node.

Data connections to the CVM8 fall into two categories: Superrate and Subrate. Superrate data connections are aggregates of channels that function as a single logical connection. These may be any multiple of 56 or 64 Kbps up to a maximum of 8 (512 Kbps). The DS0 timeslots must be contiguous or alternating, but they cannot be random. All 56 Kbps data channels are bit-stuffed up to 64 Kbps on circuit lines. The CVM8 removes the stuffing bits prior to packetization. A Superrate connection carries no supervisory bits.

Subrate data connections transmit at speeds less than 64 Kbps and are carried within a DS0. Allowable rates are: 2.4, 4.8, 9.6, and 56 Kbps. These are referred to as DS0-A connections.

In-band DS0A link codes are translated into EIA control lead states for CVM8-to-HDM or LDM connections, but fast EIA, DFM, and isochronous clocking are not available as with the HDM and LDM data cards.

## BC-J1 Description

The BC-J1 back card provides a Japanese J1 circuit line interface for a CVM8 card. The BC-J1 back card plugs directly into the P2 connector on the CVM8 front card. The card pair can be installed in any slot 3 to 8 in an IGX 8. The BC-J1 features the following:

- Interfaces to Japanese TTC (J1) lines specified by JJ-20-10, JJ-20-11, and JJ-20-12
- Supports both Channel Associated Signalling and Common Channel Signalling
- Supports 30 channel, 2.048 Mbps operation
- Uses Coded Mark Inversion (CMI) line coding
- Maintains statistics for J1 line events (Frame loss, Loss of signal, frame errors, etc.)
- In response to certain line alarms, automatically starts local loopback tests

The BC-J1 supports two clock modes. These are *normal clocking* and *loop timing*. The system operator selects the mode through software control. Normal clocking uses the receive clock from the network for incoming data and supplies the transmit clock for outgoing data. This clock can be used to synchronize the node.

Loop timing uses the receive clock from the network for the incoming data and turns the receive clock around for timing the transmit data.

Figure 2-22 shows a BC-J1 faceplate. Table 2-15 lists BC-J1 connections and status LEDs.

**Figure 2-22 BC-J1 Faceplate**



**Table 2-15 BC-J1 Connections and Status LEDs**

Connector/Indicator	Function
RX-TX	Female DB15 connector for XMT and RCV J1.
Line in	J1 trunk input line
Line out	J1 trunk output line
RX MON	BNC test connector for monitoring receive J1 line.
TX MON	BNC test connector for monitoring transmit J1 line.
<b>LOS</b> light (red)	Indicates loss of signal at the local end.
<b>Red Alarm</b> light (red)	Indicates loss of local frame alignment.
<b>Yellow Alarm</b> light (yellow)	Indicates loss of frame alignment at remote end.
<b>AIS</b> light (green)	Indicates the presence of all ones on the line.
<b>MFRA</b> light (red)	Indicates loss of multiframe alignment
<b>MFYA</b> light (yellow)	Indicates loss of multiframe at the remote end
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
<b>ACTIVE</b> light (green)	Indicates that the card is in service and that active circuits are present.

## Frame Relay Cards

Frame relay services on the IGX 8 require the FRM8 front card and one of four FRI back cards. The back cards plug directly into the P2 connector of the front card. Table 2-16 shows the FRM8 front cards and possible corresponding FRI back cards. The card pair can be installed in any of slots 3 to 8. These cards and software are optimized to accommodate the bursty nature of frame relay without dedicating excessive bandwidth to each circuit.

**Table 2-16 FRM8-FRI Application**

Front Card	Back Card	Model
FRM8—Frame Relay Module	FRI-V.35	B
	FRI-X.21	A
FRM8-6—Frame Relay Module, w/6 chan.	FRI-T1 or FRI-E1	A
FRM8-31—Frame Relay Module, w/31 chan.	FRI-T1 or FRI-E1	A

The IGX 8 frame relay network features a Permanent Virtual Circuit (PVC) Service and a standards-based protocol Local Management Interface (LMI) for facilitating the administration and maintenance of the network. Two protocols are used: a data transfer protocol operates end-to-end through the network, and an LMI control protocol operates between the user device and the network. The frame relay cards can provide a frame-forwarding service in which all valid HDLC frames are forwarded from one port to a single specified port elsewhere in the network without frame relay header processing or LMI control. Frame relay service also provides bundled connections and explicit congestion notification.

### Frame Relay Module 8 (FRM8)

The Frame Relay Module 8 (FRM8) front card is a programmable communications processor that can support one-to-four data ports and, in a single port mode, operate at speeds up to 2.048 Mbps. The FRM8 operates in conjunction with the appropriate FRI back card as a card set.

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**Note** The FRM8 front card may be either a two-piece version or a single-card version. The two-piece version is a modified FRP with an ACM adapter card.

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### FRM8 Features and Functions

The FRM8 supports the following features.

- Bundled connections
- Frame forwarding
- GMT request/response
- Explicit Congestion Notification (ECN)
- Multicast services
- Maximum port speed up to 2.048 Mbps
- ForeSight dynamic congestion avoidance
- AIP applications
- Enhanced V.35 loopback test
- T1 and E1 frame relay port interfaces
- Network-to-Network Interface (NNI) ports for V.35 and X.21 frame relay connections that extend across a StrataCom network to a foreign network
- Network-to-Network Interface (NNI) ports for T1 and E1 frame relay connections
- Frame forwarding of frame-like data (HDLC, for example)

The FRM8 can process a maximum of 252 virtual circuits (PVCs). The PVCs may be distributed over the four ports in any manner as long as the total of 252 PVCs and a limit of 2.048 Mbps per FRM8 card is not exceeded. Bundled and grouped connections are software groupings of multiple virtual circuits within a single routing connection. This allows a single node to support up to 1024 virtual circuits, equivalent to four FRM8s.

The FRM8 front card:

- Packetizes and depacketizes frame relay data frames.
- Provides frame multiplexing/demultiplexing using the DLCI field.
- Regulates the flow of data frames into the network to provide adequate congestion prevention and control.
- Inspects frames to ensure that they are not too long or too short and that they consist of an integer number of octets prior to zero bit insertion or following zero bit extraction.

- Performs CRC check to detect transmission errors.
- Supports intelligent communication with user devices to share information on addition, deletion, congestion, and alarms of the multiple PVCs on each port.
- Performs and reports the results of the loopback tests for internal, local external, and remote external tests to modems or NTUs.

The FRM8 converts frame relay data into FastPackets while remaining transparent to the user data. Frame delimiting and transparency are provided by the HDLC flag bytes and bit stuffing. Valid hex 7E data bytes are converted using zero bit insertion such that they are not mistakenly taken as flag bytes.

On the receiving end, all frames are checked against the embedded Frame Check Sequence (FCS) code. A frame that does not pass is discarded. Flag bytes that are used to fill up any partially filled FastPacket frames are also discarded.

A hardware jumper position selects the clock for V.35 and X.21 interfaces. The clock is selected when the jumper position puts the back card in DTE mode. The FRM8 measures and reports the actual clock speed to the system software.

## Types of FRM8s

Three types of FRM8s are available. They are the FRM8, FRM8-6 and FRM8-31. The distinction between these three is as follows:

- The FRM8 is the non-channelized version and supports V.35 and X.21.
- The FRM8-6 is a channelized version with 6 channels and supports T1 and E1 connections.
- The FRM8-31 is a channelized version with 31 channels and supports T1 and E1 connections.

## Frame Relay Interface (FRI) V.35 Card

The Frame Relay Interface V.35 (FRI-V.35) is a four-port back card to the FRM8 card. Two models of the FRI-V.35 can support the FRM8:

- FRI-V.35 Model A—supports composite data rates up to 1.024 Mbps and provides a CCITT V.35 interface for each port.
- FRI-V.35 Model B—supports composite data rates up to 2.048 Mbps with V.35 interface

The FRI-V.35 has the following functions and features:

- Interfaces 1–4 frame relay ports via 34-pin MRAC connectors (Winchester, female)
- RTS, CTS, DSR, DTR, DCD, LLB, RLB, and TM control leads supported
- Handles up to 252 virtual circuits per card
- Hardware jumpers to configure the interface as DCE or DTE
- Supports normal and looped clocking

**Figure 2-23      Frame Relay V.35 Connectors and Indicators**

Table 2-17 shows the relationship between the number of ports used on the FRI and maximum operating speed for each port. Model A FRM8 and FRI cards are included for early users who may not have updated the cards. Note that the port numbers start at the top on the FRI faceplate.

**Table 2-17**      **Frame Relay Port Speed Combinations**

<b>FRM8/FRI MODELS</b>	<b>PORT 1</b>	<b>PORT 2</b>	<b>PORT 3</b>	<b>PORT 4</b>
A/A	≤ 256	≤ 256	≤ 256	≤ 256
	1024	0	0	0
B/A	≤ 256	≤ 256	≤ 256	≤ 256
	≤ 336	≤ 336	≤ 336	0
	≤ 512	≤ 512	0	0
	1024	0	0	0
C/A	≤ 256	≤ 256	≤ 256	≤ 256
	≤ 336	≤ 336	≤ 336	0
	≤ 512	≤ 512	0	0
	1024	0	0	0
C/B D/B	One port can operate at 2048 or 1920. Combinations of ports can equal 2048 Kbps. If one port is 1920 or 2048, no other port may be active.			

## Frame Relay V.35 Port Numbering

Each frame relay port is assigned a number in the form:

SLOT.PORT.DLCI

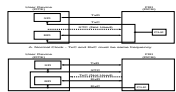
where SLOT is the slot number where the FRM8/FRI-V.35 resides; PORT is the physical port in the range of 1–4; and DLCI is the identification number for the PVC. The range of valid DLCIs is 16–1007. DLCIs 0–15 and 1008–1023 are reserved.

## FRI-V.35 Data Clocking

The two clocking modes that the FRI supports are *normal* and *looped* (see Figure 2-24). Note that the direction for the clock and data are reversed for the two FRI mode configurations (DCE or DTE), as follows:

- If the FRI operates as a DCE with normal timing, it provides both transmit and receive clocks to the user device.
- If the FRI operates as a DTE with normal timing, the user device provides both transmit and receive clock.
- If the FRI operates as a DCE with looped timing, the user device provides the transmit clock on the EXT XMT CLK line, and the FRI provides the receive clock to the user device.
- If the FRI operates as a DTE with looped timing, it provides the transmit clock on the EXT XMT CLK line, and the user device provides the receive clock.

**Figure 2-24**      **Frame Relay Data Clocking Modes**




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**Note** Clock is looped by the FRI instead of the user device.

---

## Frame Relay Over T1 and E1 Lines

Implementing frame relay on a T1 or E1 line requires:

- FRP model E firmware
- FRI-T1 back card or FRI-E1 back card, respectively

A Frame Relay T1/E1 connection can terminate on any Frame Relay Interface—V.35, X.21, T1 or E1.

Frame Relay T1/E1 allows the user to group FRM8 DS0 timeslots into “logical ports.” These logical ports are groups of contiguous DS0s or a single DS0. Logical ports that consist of multiple DS0s are at the full rate of 64 kbps per timeslot. Logical ports that consist of single DS0s may be configured as 56 Kbps or 64 Kbps. See Figure 2-25.

If a logical port is configured for 56 Kbps, the IGX 8 strips the least significant (signalling) bit from the incoming octet and puts a 1 in the least significant bit of the outgoing octet. This 56-Kbps rate typically applies to groomed DDS circuits that exist on a T1/E1 line.



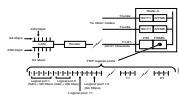
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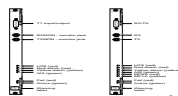
**Note** E1 reserves timeslot 16 for special use, so timeslot 16 is unavailable with channel-associated signalling (CAS). However, with common channel signalling (CCS), timeslot 16 carries data. See **cnfcln** in the *Command Reference*.

---

The features available for existing frame relay applications (such as LMI and 252 PVCs) and a full set of commands to gather statistics are also available for Frame Relay T1/E1. (Exceptions to this are external port loopbacks and signalling capability.)

**Figure 2-25 Multiple and Single DS0s Forming a Logical Port**





## Loopbacks

Port loopbacks (**tstport** and **addextlp** command) towards the IGX are not supported. Port loopbacks towards the facility are supported using the **addloclp** and **addrmtlp** command.

## Supervisory Signalling

Except for LMI, logical ports do not support supervisory signalling. No EIA leads or signalling bits as used in a V.35 port. The commands **cnfict**, **cpyict**, **dspict** and **prtict** are ignored unless a FRI V.35 back card is present. The **dspbob** command is blocked when executed on a logical port.

## FRM8-FRI Compatibility

The Display Card (**dspcd**) command indicates the type of back card that the FRM8 firmware supports and reveals a mismatch.

## Commands for T1/E1 Frame Relay

Logical ports are created with the **addfrport** command, which associates a group of DS0s to a logical port number. The lowest timeslot number of the created group becomes the logical port number. The logical port number is used to *up* the port (**upfrport**), add connections (**addcon**) and display statistics (**dspportstats**).

Logical ports are displayed with the **dspfrport** command and are arranged in ascending port number sequence.

Logical ports are deleted using the **delfrport** command, which will ungroup any multiple DS0s and/or unassign a single DS0 logical port. (Connections for the ports must be deleted (**delcon**, **delcons**) prior to deleting the ports.) After deleting one or more ports, downing the port (**dnfrport**) can follow.

## Frame Relay Interface for X.21

The FRI - X.21 is a back card used with the FRM8 front card to provide the X.21 interface to the IGX network. Each FRI connects directly to a corresponding FRM8 card.

Two different model D FRI cards are available: the FRI-V.35 and FRI-X.21. They differ only in the physical interface provided. The operating rates of each port and the composite data rate supported by the FRI-X.21 card are the same as the present frame relay V.35 card set. Each port may be configured either as a DCE or a DTE.

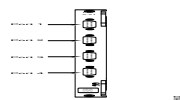
The FRI-X.21 uses leased line service for international networks. The V.35 version is for domestic US use and also is intended for connection to leased line service.

The FRI-X.21 back card has the following features:

- Four frame relay data ports with CCITT X.21 interface through DB15 connectors
- Support for all standard X.21 data rates to 2.048 Mbps
- Support for C (control) and I (indication) control leads
- Daughter board used to configure FRI as DCE or DTE.
- Card redundancy option provided by Y-cable and standby card pair

The FRI can be configured to use any combination of one through all four ports. The configuration depends on the maximum operating speed required (the card itself has a maximum composite operating speed). The four ports are numbered from top to bottom on the FRI faceplate (Figure 2-27). Table 2-18 lists the available port operating speeds.

Figure 2-27      Frame Relay X.21 Connectors and Indicators



Any one port may operate at 2048 Kbps. Any combination of ports may equal 2048 Kbps. If one port is operating at 2048 Kbps, it must be port 1, and no other port may be active. The four DB15 port connectors numbered from top to bottom.

Table 2-18      Frame Relay Port Speeds Available

Port Speeds (Kbps)	Maximum
Any multiple of 56 Kbps	Up to 1.792 Mbps
Any multiple of 64 Kbps	Up to 2.048 Mbps

Table 2-19 lists the cable and pinouts used for X.21 ports.

**Table 2-19 FRI-X.21 Port Pin Assignments (DB 15-connector)**

Pin #	Name	Source	Description
1	Gnd.	both	Protective (Shield) ground
8	Sig. Gnd.	both	Signal ground
3/10	C	both	Control
5/12	I	both	Indication
2/9	TxD	DTE	Transmit Data from DTE
4/11	RxD	DCE	Receive Data to DTE
6/13	S	DCE	Clock Out (DCE), Clock In (DTE)

## X.21 Data Clocking

The FRI-X.21 supports only a normal clock mode. The direction of the clock and data lines is reversed if the FRI is configured as a DCE or as a DTE as follows (see Figure 2-28):

- If the FRI is configured as a DCE, it provides a clock signal to the user device (DTE) on the S (clock) lead. This clock is synchronous with the node timing.
- If the FRI is configured as a DTE, the FRI-X.21 receives clock from the user device (DCE) on the S (clock) lead.

**Figure 2-28 Frame Relay Data Clocking Modes**

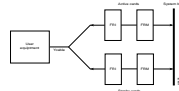


## Frame Relay Card Redundancy

Optional redundancy for the frame relay card types (FRM8/FRI) can be provided with a second front and rear card set and a Y-cable connection on each port to the customer data equipment as illustrated in Figure 2-29. A card redundancy kit is available. The kit contains a second FRM8/FRI card set, a set of four Y-cables to interconnect the two card sets to the customer connection, and a redundant DCE/DTE programming board for the installed FRI. In the case of either a V.35 or X.21 interface, when a non-redundant card set is changed to a redundant card set, the DCE/DTE daughterboard on the FRI must be changed. Only the model D of the V.35 or X.21 back cards use the daughterboard.

Y cable redundancy is not possible between differing back card types, such as FRI T1/E1 to FRI V.35.

After the hardware is installed, the node must be reconfigured to indicate that the slot is equipped with redundancy. Use the Add Y Redundancy (**addyred**) command to do this. Refer to the *Command Reference* for more information on the redundancy commands **addyred**, **delyred**, **dspyred**, and **ptyred**.

**Figure 2-29 Frame Relay Port Redundancy**

## Card Self Test

As with all IGX cards, the FRI-X.21 includes internal diagnostic routines that periodically test the card's performance. These self-test diagnostics automatically start and run in background. They do not disrupt normal traffic. If a failure is detected during the self test, the faceplate red FAIL LED is turned on. The operator can also view the status at the control terminal by executing the Display Card (**dspcd**) command.

A report of a card failure remains until cleared. A card failure is cleared by the Reset Card (**resetcd**) command. The two types of reset that **resetcd** can do are hardware and failure. The failure reset clears the event log of any failure detected by the card self-test but does not disrupt operation of the card. A reset of the card firmware is done by specifying a hardware reset. This reboots the firmware and momentarily disables the card. If a redundant card is available, the hardware reset causes a switch over to the standby card.

## Port Testing (X.21)

The X.21 frame relay ports and any associated external modems, CSUs, or NTUs can be tested using data loopback points in the circuit path. There are three possible loopbacks for X.21 frame relay ports that may be established, as follows:

- An internal loopback of the port
- A loopback of the near end (local) modem
- A loopback of the other end (remote) modem

The modems must be compatible with the StrataCom loopback protocol. For information on supported modems and protocols, refer to the *IGX 8 Installation Guide* and the *Command Reference* for protocol requirements for the **addextlp**, **addloclp**, and **addrmtlp** commands. The ISC can also answer questions about modem protocol requirements.

All three loopbacks are set up using the **tstport** command. Only one port at a time can be in loopback mode for testing.

The internal loopback point is established inside the FRI card (see Figure 2-30). The FRM8 generates a test pattern, sends it out on the transmit circuitry, and detects this pattern on the receive circuitry. This test takes several seconds and momentarily interrupts traffic on the port. The test works on ports configured for either DCE or DTE.

**Figure 2-30**      **Frame Relay Loopback Modes**



For ports configured for DTE, the two additional tests (local loopback and remote loopback) are available. For these ports, two methods of loopback testing are available:

- A loopback data pattern (Test Mode) is transmitted to initiate a loopback. The modems or NTUs may or may not recognize these codes to perform the loopback. The FRI does not care and waits a programmable time period (default = 10 secs.) to send the test pattern. After the test is completed, transmission of the codes terminates, and the circuit returns to normal operation. A Model D FRM8 is required for this testing. The test result is displayed on the IGX control terminal **tstport** screen.
- Some external equipment supports loopback testing but does not recognize the test pattern (Test Mode) in the data stream. In these cases, the FRM8/FRI toggles the V.35 LLB (local loopback) and the RLB (remote loopback) leads then runs the test pattern. The FRM8/FRI still waits the user-programmable time period (default = 10 secs) before running the data test pattern. A Model D FRM8 is required for this testing. The test result is displayed on the IGX control terminal **tstport** screen.

## Unit Replacement

Replacing an FRI card is done the same way as other back cards. For these procedures, refer to the repair and replacement description in the *IGX 8 Installation Guide*.



## Port Concentrator Shelf (PCS)

This section introduces the Port Concentrator Shelf (PCS) for frame relay traffic. The PCS is an external device that expands the capacity of a Frame Relay Module (FRM8) to 44 low-speed ports. This ability to increase the port density of an IGX 8 switch supports more efficient usage for the common switch equipment. The port parameters are as follows:

- The per-port range is 9.6 Kbps to 384 Kbps.
- The typical configured rate is 64 Kbps per port or less.
- The maximum total throughput for the 44 ports is 1,792 Kbps.

The PCS requires a version of the FRM8/FRI card set that is exclusively dedicated to the PCS. The front card is the FRM8-2. The back card that interfaces the FRM8-2 to the PCS is the FRI-2-X.21. Figure 2-31 illustrates the relationship between the FRM8/FRI card set and the PCS. It provides one or more X.21 links. Each X.21 link is called a *concentrated link*. In a full configuration, each concentrated link services one of four 11-port modules in the PCS. This makes a total of 44 ports on the FRM8-2.

For more detailed information on the PCS, refer to the section on the Port Concentrator Shelf in the *System Manual*. For detailed installation instructions, refer to the *Port Concentrator Installation* document that comes with each unit. Cabling information for the PCS appears in the cabling appendix in the *IGX 8 Installation Guide* and the *Port Concentrator Installation* document.

**Figure 2-31 Port Concentrator Shelf Components**



### Terminology

The following terms are used to identify PCS components:

- “FRC” (Frame Relay Concentrator) ports—FRM8-2-X.21 ports connected to the PCS, used in commands such as **dspfrport**. These are also known as PCS “physical” ports.
- Frame Relay “Logical” ports—each of the 44 PCS ports, from the perspective of the IGX 8 interface, is a logical port. This distinguishes it from a physical frame relay port.

### Operation and User Interface

Other than front panel LEDs, the PCS has no user interface because the PCS functions as an extension of the FRM8-2. PCS ports are operated and maintained from the IGX 8 user interface.

The PCS operates within the IGX 8 environment as a frame relay card with 44 ports. Existing frame relay commands (**cnfrport**, **upfrport**, **dnfrport**, **addcon**, **delcon**, etc.) are the same in syntax and function. The difference is that a range of 44 ports can be specified instead of 4. The configuration of each of the PCS logical ports is similar to that of non-PCS frame relay ports. A frame relay card connected to a PCS notifies the system database and permits the additional ports to be specified.

### PCS to IGX 8 Interface

#### Compatibility

The PCS requires the following:

- System software version 8.1 or later
- FRM8-2 front card and FRI-2-X.21 back card

---

**Note** Use of the PCS with the FRM8-2 is exclusive: the PCS does not work with any other model of the FRM8, and only a PCS works with the FRM8-2. For example, if fewer than four PCS modules are used, unused frame relay ports cannot connect to something other than the PCS.

---

### Concentrated Link

The concentrated link refers to the connection between port concentrator and frame relay card. Each module supporting 11 external ports is connected to 1 of the 4 ports on the FRI-2-X.21 back card.

#### Configuration

Each of the four composite links between PCS and IGX 8 has a fixed configuration of:

- Speed: 512 Kbps
- FRM8-2 physical port: FRI-2-X.21 DCE
- PCS composite port: X.24/V.11 DTE

The PCS Concentrated Link cable is illustrated in the cabling appendix. Its maximum length is 25 feet. The use of modems to extend this distance is not supported.

## Activation

FRM8-2 firmware interacts with the PCS over composite links only if the FRM8-2 is active. The FRM8-2 changes from standby to active when its first logical port is activated.

## upfrport Command

A PCS logical port associated with an FRM8-2 card is activated with the **upfrport** command. The **upfrport** command requires the slot number of the FRM8-2 to which the PCS is connected. Enter the slot number and a logical port in the range of 1-44 (assuming a connection exists for all 4 composites between the PCS and IGX 8).

Example: upfrport 4.1

This example indicates that the FRM8-2 in slot 4 and concentrated link 1 are connected.

Entering the **upfrport** command for one port activates all four ports. The following events are generated by successful activation of four concentrated links. The display is from the example “**upfrport** 4.1:”

```
“Info  FRM 4 Activated”
“Info  FRM Port 4.1 Activated
“Info  FRM Concentrated Link 4.1 Failure Cleared”
“Info  FRM Concentrated Link 4.2 Failure Cleared”
“Info  FRM Concentrated Link 4.3 Failure Cleared”
“Info  FRM Concentrated Link 4.4 Failure Cleared”
```

A noticeable delay occurs after **upfrport** begins executing on the first port. During initial **upfrport** execution, the FRM8-2 performs first-time configuration, diagnostic, and up/download functions.

If a concentrated link is not connected or fails to come up, the logical port remains in a failed state until either the link comes up or the port is deactivated with the **dnfrport** command.

## De-activation: the dnfrport Command

The frame relay card returns to the standby state after all 44 logical ports are de-activated with the **dnfrport** command.

## PCS Port Configuration

When the frame relay ports are activated, the IGX 8 recognizes them as PCS-connected ports. Subsequently, all applicable frame relay port management commands accept logical port numbers in the form *slot.port*. The range for *port* is 1–44. Table 2-20 shows the logical ports the PCS supports. In Table 2-20, *slot* is the IGX 8 slot in which the FRM8-2 resides.

**Table 2-20 PCS Logical Ports**

	PCS Ports
Composite L1	slot.1 to slot.11
Composite L2	slot.12 to slot.22
Composite L3	slot.23 to slot.33
Composite L4	slot.34 to slot.44

### Interface Hardware Configuration

The interface and clocking characteristics for each PCS port is independently configured to be V.11 (X.21), V.35, or V.28 by inserting the required interface card (or “ICARD”) into the associated slot on the PCS module. For detailed information on PCS port hardware interfaces, refer to the *Port Concentrator Installation* document. This document comes in the PCS shipping container.

The IGX 8 does not have the capacity to read the type of interface present for the PCS port. Values entered under the *Interface Type* parameter with the **cnffrport** command are used in the display only and cannot be checked against the hardware.

### Software Parameters

#### cnffrport Command

A PCS is configured with the **cnffrport** command. With the following limitations, all parameters for the **cnffrport** command are supported:

- 1 For each group of 11 logical ports, the total speed must be 384 Kbps or less. The remaining 64 Kbps of composite link speed is reserved for control information. This total includes active ports only.
- 2 The default port speed is 38.4 Kbps (instead of 256 Kbps for non-PCS ports).
- 3 Speeds of 9.6, 14.4, 16, 19.2, 32, 38.4, and 48 Kbps are supported on PCS ports. Higher speeds (56 - 384 Kbps) are valid as long as limitation 1, above, is observed.
- 4 Only the active *Interface Control Template* is supported by the PCS.

### Port Statistics

All frame relay summary and interval statistics are kept for PCS ports.

The PCS and FRM8-2 share responsibility for statistics collection on PCS ports. The PCS maintain counters for:

- CRC errors on received frames
- Aborted/underrun received frames
- Transmit Frames Discards
- Transmit Bytes Discards

All remaining statistics counters are collected by the FRM8-2. Although the IGX 8 supports ForeSight for PCS connections, CLLM (ForeSight) statistics are not available in Release 8.1. These fields are present but not valid.

### PCS Monitoring Functions

Monitoring functions generally apply to the PCS with the exception that up to 44 ports may be specified per FRM8-2 slot. For descriptions of the monitoring commands, refer to the “Troubleshooting” chapter of the *Command Reference*. Note that commands **dspchcnf**, **dspchstats**, **dspportstats**, and **dspbob** fail when the required concentrated link is down. Trying to execute one of these commands on a concentrated link that is down causes an error message to appear.

## Collection of Monitoring Information

### Logical Port Speed

The PCS measures the speed of receive data on logical ports if the port is configured as a DTE interface. To see the measured speed, use the **dspbob** command. The PCS measures port speed after any of the following occurs:

- When the PCS is first powered up.
- When a port is configured with the **cnffrport** command. After the port is reconfigured, speed of incoming data is measured.
- When signals are displayed for the port with the **dspbob** command, you are given the option of measuring port speed at that time.
- When the PCS is reset or power is interrupted for any reason.

The process of measuring port speed sends out two 1-byte frames with no CRC on the port.

### Physical Port Speed

The IGX 8 measures the physical port speed for FRI-2-X.21 ports once per minute. The current measured speed is displayed with the **dspfrport** command and should always read 512 Kbps when the port is active.

## PCS Front Panel LEDs

**Table 2-21 PCS Module Front Panel LEDs**

LED Name	Function
State	OFF = No power. RED = power applied but software not running. GREEN = Operating software running.
Rx	Flashes for frames received on concentrated link.  With no activity on link, Link Rx blinks once per second. With steady activity on link, Link Rx is on continuously.
Tx	Flashes for frames transmitted on concentrated link  With no activity on link, Link Tx blinks once per second. With steady activity on link, Link Tx is on continuously.
CPU A	On solid when operating software on CPU A is running. Should always be On during operation.
CPU B	On solid when operating software on CPU B is running. Should always be On during operation.
Port 1	On for module connected to FRM8-2 Port 1. Should always be On for Module 1.
Port 2	On for module connected to FRM8-2 Port 2. Should always be On for Module 2.
Port 3	On for module connected to FRM8-2 Port 3. Should always be On for Module 3.
Port 4	On for module connected to FRM8-2 Port 4. Should always be On for Module 4.
Download	Flashes when frames are received during software download from FRM-2.

## Card Insertion and Removal

If the FRM8-2 or FRI-2.X21 card is removed for any reason, be sure to maintain card compatibility upon card replacement: the FRM8-2 card is compatible with *only* the FRI-2-X.21 back card. The IGX 8 declares a mismatch state for any other back card inserted into an active FRM8-2 slot. Inserting compatible hardware is the only way to clear the mismatch. Similarly, once an IGX 8 slot is active with an FRM8-2, a mismatch is declared if any other front card is inserted into this slot. Before the slot can be used for any other type of card, the slot must be de-activated as a PCS-capable frame relay card.

## PCS Command Summary

The commands in the list that follows apply to PCS frame relay ports. Most commands have the syntax described in the *Command Reference* with the exception that the system software recognizes 44 ports per slot for a PCS instead of 4. Some commands are PCS-specific.

<b>addcon</b>	used to add frame relay connections.
<b>addloclp</b>	test command for creating local loopback.
<b>addrmtlp</b>	test command for creating remote loopback.
<b>cnfcondsc</b>	configures frame relay connection description.
<b>cnfchpri</b>	configures frame relay channel priority.
<b>cnfchutl</b>	optimizes frame relay channel utilization.
<b>cnffrcsl</b>	configures frame relay class of service (also <b>cnfcos</b> command for connection).
<b>cnffrcon</b>	configures frame relay connection parameters.
<b>cnffrport</b>	configures frame relay port parameters.
<b>cnfpref</b>	configures preferred route for frame relay connection.
<b>clrchstats</b>	clears PCS frame relay channel statistics.
<b>clrfrcportstats</b>	PCS-specific command for clearing FRI-2-X.21 physical port statistics.
<b>clrportstats</b>	clears PCS frame relay port statistics.
<b>delcon</b>	deletes a frame relay connection.
<b>delcongrp</b>	deletes a frame relay connection group.
<b>dellp</b>	deletes a frame relay loopback condition.
<b>dncon</b>	deactivates a frame relay connection.
<b>dspchcnf</b>	displays channel configuration.
<b>dspchstats</b>	displays PCS frame relay channel statistics.
<b>dspfrcportstats</b>	PCS-specific command to display FRI-2-X.21 physical port statistics.

<b>dspfrcbob</b>	PCS-specific command for displaying FRI-2-X.21 physical port signals.
<b>dspfrport</b>	displays PCS logical port.
<b>dspfreport</b>	displays FRI-2-X.21 physical port.
<b>dsppcs</b>	PCS-specific command to display PCS unit information.
<b>dspportstats</b>	displays PCS frame relay port statistics.
<b>dspcon</b>	displays frame relay connections.
<b>prtcons</b>	prints frame relay connections.
<b>prtchcnf</b>	prints frame relay channel configuration.
<b>tstcon</b>	tests frame relay connection.
<b>tstdelay</b>	tests frame relay connection delay.
<b>tstpc</b>	PCS-specific command to test the PCS.
<b>resetpc</b>	PCS-specific command to reset the PCS.

## PCS Port Failures

A PCS logical port failure is defined as a Minor alarm. The *FTC/FRP Port Comm Failure* icon appears in the *dspalms* screen. Any connection that terminates on a failed port is also failed. Three causes of a port failure are defined, as described under Alarms and Events.

## Conditioning

A failed connection on a PCS logical port is conditioned in the same manner as a failed connection on a non-PCS FRP port. Only the active control template is supported on PCS ports. The “conditioned” control template should not be used for PCS logical ports.

## PCS General Operation

### Firmware Download

When it detects a Port Concentrator on one of its links, the FRM8-2 checks for a compatible firmware revision on the Port Concentrator. If the FRM8-2 detects that the current firmware on the Port Concentrator is incompatible, the FRM8-2 downloads the current firmware to the Port Concentrator. This download operation takes about two minutes. An event is logged when a firmware download has either started or failed.

Operating software on the Port Concentrator is stored in Flash memory. Download should be required only if the PCS is connected to an FRM8-2 with newer firmware, or a PCS module is replaced and a software version difference exists.

### Automatic Diagnostics—FRM8-2 and FRI-2-X.21 Cards

The FRM8-2 card runs a self-test diagnostic when it is in the standby state. The system software uses a reserved channel on the FRM8-2 card to perform background loopback tests that include both the FRM8-2 and FRI-2-X.21. This test verifies that all components up to the FRI-2-X.21 physical port are functioning. These diagnostics do not test the PCS.

### StrataView Plus Interface

Information about PCS logical ports and frame relay connections is automatically reported to StrataView Plus, just as they are for FRP ports and connections.

Connection Management and other FRM8-2 port functions may also be handled for PCS ports from StrataView Plus.

### SNMP Manager

The SNMP agent supports Port Concentrator logical ports. This includes configuring PCS port parameters, adding, or deleting frame relay connections, and retrieving statistics.

The SNMP agent also supports provisioning for 44 frame relay ports for FRM8-2; the existing MIB variables are extended to the expanded number of logical ports. SNMP management functions are not supported for the Port Concentrator concentrated links.

### User Interface

Interaction between the FRM8-2 and PCS automatically updates the database to display the number of connected logical FRM8-2 ports at the PCS. As a result, both the IGX 8 user interface and the StrataView Plus interface automatically display the additional capacity of 44 ports for the FRM8-2.

### Concentrated Link Failure

If, during normal operation, communication stops between FRM8-2 and PCS over a concentrated link, a concentrated link failure alarm is generated.

In addition, during startup, a concentrated link is failed for any of the following reasons:

- Port Concentrator is not present.
- Code download from FRM8-2 to Port Concentrator fails.
- Port Concentrator self-test fails.
- Cable is unplugged or bad.

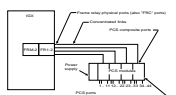
## FastPAD Cards

The IGX 8 supports the use of FastPAD devices in the network. The cards that support FastPADs for an IGX are the FTM front card and an FPC back card. The back card provides either a T1, E1, V.35, or X.21 interface. Each FPC V.35 and FPC X.21 provides four ports, and each port can support one attached FastPAD device either locally or remotely via modem. The T1 card has a DB15 for RX/TX and an alternate pair of RX/TX BNC connectors. The E1 connections are the same except for additional RX/TX monitoring BNC connectors. Dual card sets configured for Y-cable redundancy are also supported. Figure 2-32 shows the FTM front card and the V.35 version of the FPC.



Commands entered at the control terminal manage the FTM/FPC, the FastPAD, and their ports and connections. Furthermore, statistics related to cards, ports, and the FastPAD are collected and made available to StrataView Plus.

**Figure 2-32 FastPAD Cards: FTM and FPC (V.35)**



Card management of the FTM/FPC card set includes detection of card installation and removal, card mismatch, and Y-cable redundancy.

Port management includes EIA signalling, LMI alarms, upping and downing of ports and the collection of port statistics available to StrataView Plus.

FastPAD management permits the management of cards and ports on the FastPAD device from the IGX 8 node. This management includes card and card removal detection, card mismatch, uploads and downloads between the FastPAD and the IGX 8 node.

Connection management involves mapping FastPAD connection to Frame Relay type VCs (virtual circuits). Connections originating at a FastPAD must terminate at another FastPAD. Each FTM/FPC card set supports up to 252 connections. VC statistics are collected and made available to StrataView Plus.

For descriptions of the FastPAD commands and detailed information on the FastPAD, refer to the *FastPAD User's Guide*. Refer also to the *StrataView FastPAD User's Guide*.

## Data Cards

Data circuits are directly interfaced to the IGX 8 using the High-speed Data Module (HDM) or Low-speed Data Module (LDM) card sets. The HDM set consists of a HDM front card and a Synchronous Data Interface (SDI) back card. The LDM set consists of a LDM front card and a Low-speed Data Interface (LDI) back card. The back cards are used to match the circuit type to the front card. Synchronous data card sets are listed in Table 2-22. An IGX 8 can have up to 6 HDM/LDM sets in a non-redundant system, for support of up to 48 full-duplex data connections.

The synchronous data cards support the ability to configure and monitor EIA leads; the ability to configure each channel for clocking, data rate, and DTE or DCE interface type; and complete loopback testing capability. Data channels can support null modem emulation as well as constant-carrier and switched-carrier operation. Data interfaces are transparent with respect to protocol. Asynchronous, binary synchronous, and bit synchronous protocols are supported with no impact on host or terminal software.

**Table 2-22      Synchronous Data Cards Available**

Front Card	Back Card
HDM, High-speed Data Module	SDI, RS-449 synchronous data interface (for X.21 or RS-422).
	SDI, V.35 synchronous data interface.
	SDI, RS-232D synchronous data interface.
	SDI, RS-232C synchronous data interface (for V.24).
LDM, Low -speed Data Module	LDI, RS-232C (V.24) four-port and eight-port low-speed data interface, non-interleaved EIA. 4-port models limited to 56 Kbps., 8-port models up to 19.2 Kbps.
	LDI, RS-232D four-port and 8-port, low-speed data interface, up to 19.2 Kbps., non-interleaved EIA.
	LDI4/DDS, four-port AT&T 56 Kbps. Digital Data Service interface.

### High-speed Data Module (HDM)

The HDM front card in an IGX 8 is a programmable communications processor that can support one to four high speed, synchronous data channels. It operates at speeds from 1.2 Kbps up to 1.024 Mbps on all four ports while performing link error monitoring.

The HDM front data card:

- Performs cell adaptation
- Supports normal, looped, and split clocking
- Provides isochronous clocking circuitry
- Packetizes and depacketizes EIA lead sampling information
- Provides loopback capabilities, testing, and diagnostics

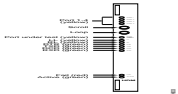
An internal baud rate generator provides transmit and receive data clocks to the SDI card at the selected rate. The HDM can accept data from an external data device with a non network synchronized clock (isochronous clock) up to 112 Kbps. With isochronous clocking, the HDM sends a clock control signal to the other end of the circuit to synchronize the far end receive clock to the isochronous clock received at the near end.

Unless specified, a packet of data for EIA control lead information is built only at a very low rate or when a change of state is detected on one or more of the control leads. The data rate is specified as either “fast” or “not fast” (the default) by the **addcon** command for data connections. A fast EIA lead transmission can be specified in the software to send EIA control lead information in every FastPacket (interleaved EIA mode). This tightly couples the EIA lead states with the transmitted data but reduces the bandwidth efficiency.

The HDM card is installed in a front slot. An SDI back card plugs directly into the P2 connector of the front card. The SDI back card provides the proper data channel interface.

The HDM faceplate has message lights and push-buttons for monitoring and loopback control. The push-buttons relate to loopback testing and for scrolling through the FastPacket data ports for a snapshot of selected port conditions (indicated by PORT, PORT UNDER TEST, loopback, and communication line state lights). Figure 2-33 shows and Table 2-23 lists the controls and indicators. When correlating the figure and table, read them top-down.

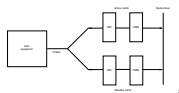
Figure 2-33      HDM Controls and Indicators



**Table 2-23 HDM Controls and Indicators**

<b>Indicator</b>	<b>Function</b>
<b>PORT</b> light (yellow)	Indicates which FastPacket data port on the back card is currently monitored.
<b>SCROLL</b> push-button	When pressed toggles through to the desired port. Information that is displayed by the remaining lights on the HDM faceplate applies to the port whose corresponding light is on.
<b>LOOPBACK</b> push-button	When pressed, toggles through the three loopback states—no loopback, local loopback, and remote loopback—on the port whose corresponding light is on. This button can be disabled by the <b>cnffunc</b> command.
<b>PORT UNDER TEST</b> light (yellow)	Indicates that one of the ports has been placed in the loopback mode. If this is not the current port, press the <b>SCROLL</b> push-button to toggle in the port being tested.
<b>LL</b> light (yellow)	Indicates that one of the ports has a local loopback.
<b>RL</b> light (yellow)	Indicates that a remote loopback is occurring on one of the ports.
<b>DTR</b> light (green)	Indicates that the Data Terminal Ready signal is ON at the selected port terminal.
<b>TXD</b> light (green)	Indicates that the Transmit Data signal is ON at the selected port terminal.
<b>DCD</b> light (green)	Indicates that the Data Carrier Detect signal is ON at the selected port terminal.
<b>RXD</b> light (green)	Indicates that the Receive Data signal is ON at the selected port.
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
<b>ACTIVE</b> light (green)	Indicates that the card is active and functioning normally.

Optional redundancy for HDM data card type can be provided by a second front and back card set and a Y-cable on each port to the customer data equipment. See Figure 2-34.

**Figure 2-34 HDM Data Port Redundancy**

## Synchronous Data Interface Card (SDI)

The SDI card is a synchronous data interface back card that connects to a front HDM card. Each SDI card has four connectors for its four data ports. Each port is independently configurable for DTE or DCE mode, baud rate, and so on. One-for-one port redundancy is provided with a second card set and standard Y-cable. In summary, the SDI card:

- Provides four ports for interfacing to the data equipment.
- Provides EIA control circuitry and samples EIA lead information.
- Performs serial-to-parallel conversion of data.
- Provides jumper attachments for configuring ports as DTE or DCE.

Four types of SDI back cards can provide an interface between an HDM front card and the customer data equipment. Table 2-24 distinguishes each type of SDI card.

**Table 2-24 SDI Physical Interfaces**

Interface	No. of Ports	Connector Type
RS-232C/V.24	4-ports	DB25 Subminiature, female
RS-232D/V.24	4-ports	DB25 Subminiature, female
RS-449/X.21	4-ports	DB37 Subminiature, female
V.35	4-ports	34-pin MRAC type (Winchester), female

Three clocking modes are available on the SDI for clocking in transmit data and clocking out receive data. In addition, the SDI can operate as either a DCE or DTE, which makes possible six combinations of clocking (See Figure 2-35 and Figure 2-36). With loop clocking, the user device must loop the RxC to the XTC for clocking out the transmit data.

When the SDI is configured as DTE, the user device is the source of clock timing and is generally not synchronous with the network (IGX) timing. This is *isochronous* clocking. Isochronous clocking allows the customer data sets at each end of a circuit to operate at slightly different rates (non-synchronously) with minimum delay and loss of data. This feature limits the amount of data allowed to accumulate in the HDM receive buffers and forces a re-synchronization before the delay reaches an unacceptable level.



Figure 2-36 SDI (as DTE) Clocking Modes





Isochronous clocking is only acceptable for data rates of 112 Kbps or less and may be used only on one input at a time per port. It does not support two isochronous clock inputs in the same direction as would be required by some modems that output both the TxC and RxC clocks totally independently.

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**Note** The IGX 8 automatically uses isochronous mode when the DTE/DCE endpoints warrant its use.

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Split clocking mode uses the user device timing for timing data transmission in one direction and the IGX 8 timing for the other direction.

## Low Speed Data Module (LDM)

The LDM front card in an IGX 8 is a programmable communications processor that can support up to eight synchronous or asynchronous data ports. Each port is independently configurable for DTE or DCE mode, baud rate, and so on. The LDM card is a low speed data module for use on RS-232C ports with data rates up to 19.2 Kbps where the higher speed capabilities of an HDM are not required. An LDM data card, when used with a LDI4/DDS, can provide 56 Kbps Digital Data Service interfaces to the IGX.

The LDM can process either synchronous or non-synchronous data inputs. With non-synchronous inputs, the data is over sampled at a rate determined by how much data jitter the customer equipment can tolerate. Or the customer may elect to use an external device to convert the asynchronous data to synchronize before it is input to the IGX.

The LDM front data card:

- Performs cell adaptation of customer data and EIA control leads.
- Supports normal and looped clocking.
- Provides loopback capabilities, testing and diagnostics.

Additional features such as embedded (fast) EIA, sixth EIA lead support, and pleisochronous clocking are also supported. The fast EIA control lead allows the user to specify including the RTS/CTS EIA control leads in the same FastPacket as customer data. The EIA control lead status is encoded as the eighth data bit in each data byte. This provides a quick EIA response without significantly affecting bandwidth requirements. It is limited to data rates of 19.2 Kbps and below.

The LDM can be installed in any empty front slot and requires an LDI back card. The LDI card plugs directly into the P2 connector of the LDM card.

The faceplate of the LDM has message lights and push-buttons for loopback control and signal monitoring. Figure 2-37 shows and Table 2-25 lists these indicators and buttons. When correlating the figure to the table, read from the top down. The buttons are for loopback testing and for scrolling through the FastPacket data ports to obtain a snapshot of selected data port conditions (indicated by PORT, PORT UNDER TEST, loopback, and communication line state lights).

Figure 2-37 LDM Connections and Indicators

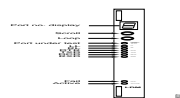


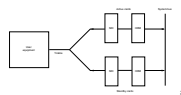
Table 2-25 LDM Connections and Indicators

Indicator	Function
<b>PORT</b> Readout Display	Indicates which FastPacket data port (1 through 8) on the Back card is currently under scrutiny.
<b>SCROLL</b> push-button	When pressed, toggles through to the desired port. Information that is displayed by the remaining lights on the LDM faceplate applies to the port whose light is on.
<b>LOOPBACK</b> push-button	When pressed, toggles through the three loopback states on the port whose light is on. The three states are: no loopback, local loopback, and remote loopback.
<b>PORT UNDER TEST</b> light (yellow)	Indicates that one of the ports has been placed in the loopback mode. If this is not the current port, press the <b>SCROLL</b> push-button to toggle in the port being tested.
<b>LL</b> light (yellow)	Indicates that a local loopback is occurring on one of the ports.
<b>RL</b> light (yellow)	Indicates a remote loopback is occurring on one a port.
<b>DTR</b> light (green)	Indicates that the Data Terminal Ready signal is ON at the selected port terminal.
<b>TXD</b> light (green)	Indicates that the Transmit Data signal is ON at the selected port terminal.
<b>DCD</b> light (green)	Indicates that the Data Carrier Detect signal is ON at the selected port terminal.

Indicator	Function
<b>RXD</b> light (green)	Indicates that the Receive Data signal is ON at the selected port.
<b>FAIL</b> light (red)	Indicates an error occurred. Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
<b>ACTIVE</b> light (green)	Indicates that the card is active and functioning normally.

Optional redundancy for LDM data card types can be provided with a second front and back data card set and a Y-cable connection on each port to the customer data equipment as indicated in Figure 2-38.

**Figure 2-38 LDM Data Port Redundancy**



The 4-port and 8-port LDM supports only a subset of the full RS-232C/D control leads. The LDM supports only non-isochronous DCE normal and DCE or DTE looped clocking modes, transmission of 3 EIA lead states (non-interleaved), and baud rates of up to 19.2 Kbps on the 8-port version and 38.4 Kbps on the 4-port version. Split clock mode is not supported.

## DDS Using the LDM/LDI

An LDI4/DDS card provides an interface for four 56 Kbps DDS circuits for T1 IGX 8 systems. The LDI4/DDS card can operate either as a Data Service Unit (DSU) or Office Channel Unit (OCU). Again, one for one port redundancy is available with a second card set and Y-cables for each port.

The LDI4/DDS operating parameters are software selectable for either DSU or OCU mode. Permissible operating speeds in Kbps are: 2.4, 4.8, 9.6, 19.2, and 56. The clock synchronization is looped clock for the OCU mode and external clock for the DSU mode. The LDI4/DDS supports the DDS control codes idle, zero suppression, out-of-service, and loopback sequences. Standard DDS diagnostic tools such as local and remote loopbacks are provided.

In the DSU mode, a port is connected to the local service provider and they should guarantee that the line length will not exceed the 34 dB loss limit that the DDS standard requires. When using a port in the OCU mode, private wiring is used. In this case, refer to Table 2-26 which shows worst case loss and maximum loop lengths for the different operating rates.

**Table 2-26 DDS Loop Length Maximums**

Line Rate (Kbps)	Loss (dB)	Equivalent Cable Length (miles)			
		19 ga.	22 ga.	24 ga.	26 ga.
2.4	42	28.7	19.8	15.6	13.3
4.8	42	21.0	14.3	11.2	9.5
9.6	42	15.8	10.4	8.1	6.8
19.2	42	12.5	7.9	6.0	5.0
56	38	8.7	5.1	3.6	2.9

### Low Speed Data Interface Card (LDI)

The Low-Speed Data Interface (LDI) card is a low-speed data interface back card that operates in conjunction with an LDM front card. The LDI provides the physical and electrical connection interface between the user low-speed data circuit and the LDM data PAD. Three models of the LDI exist. Two are four-port cards, and one is an eight-port card, as Table 2-27 indicates.

Some of the functions and features of the LDI are:

- Four or eight ports for interfacing to the data equipment
- Sampling of EIA lead status for the LDM to monitor
- Serial-to-parallel conversion of user data
- Support for DTE or DCE operation

**Table 2-27 LDI Physical Interfaces**

Unit	Interface	Ports	Connector Type
LDI 4	RS-232C/D (V.24)	4-ports	DB15 Subminiature, female
LDI 8	RS-232C/D (V.24)	8-ports	DB15 Subminiature, female
LDI 4 /DDS	DDS	4-ports	DB15 Subminiature with pigtail adapter for 8-pin RJ-48 connections

The LDI can operate either as a DCE or DTE. Selection is made by using a StrataCom DTE or DCE adapter cable between the port connector and the cable from the user device. This cable is terminated with a standard DB25 on the customer end. Each port is independently configurable for DTE or DCE mode.

Three EIA control leads are brought out to the rear connectors, three when used as a DCE and three for DTE. These leads are:

**Table 2-28 EIA Control Leads**

<b>LDI as DCE</b>	<b>LDI as DTE</b>
RTS	CTS
DSR	DTR
DCD	RL

The RL lead allows the use of a previously unused DTE EIA output lead. Remote loopback (RL) may be used to enable a far-end modem loopback. Local loopback (LL) is not provided as an output on the LDI. If local loopback is required, use the DTR lead by wiring it to the connector pin for local loopback.

The LDI supports two clocking modes: normal and looped (Figure 2-39). The normal mode is used when the LDI port is configured as a DCE. Looped clock is only used when the LDI port is configured as a DTE. The user device must take the external transmit clock and loop it back to the RxC for clocking in the receive date. In both cases, the LDI is the source of clock timing. Table 2-29 shows the accuracy and worst case jitter that can be expected from an end-to-end circuit using LDIs at each end

**Table 2-29 LDI Clocking Accuracy and Jitter Performance**

<b>Baud Rate</b>	<b>Accuracy (<math>\pm</math> Hz.)</b>	<b>Jitter (<math>\pm\%</math>)</b>
1200	.06	.03
2400	.12	.06
4800	.24	.12
9600	.48	.23
14,400	.72	.35
19,200	.96	.47

Figure 2-39 LDI Clocking Modes



## Optional Alarm Interface Cards

The alarm relay card set is optional. The set consists of an Alarm Relay Module (ARM) front card and an Alarm Relay Interface (ARI) back card. This card set provides alarm summary outputs by using relay contact closures.

The alarm outputs are typically wired to a telephone central office alarm system for remote alarm reporting to give an indication there is a problem in the associated equipment.

The alarm summary feature provided by the Alarm Relay cards provides both a faceplate visual indication of an IGX 8 node alarm as well as a set of relay outputs (dry-contact) for indicating node and network alarm indications. A visual alarm history indication is also provided. This alarm reporting is separate and is in addition to the alarm output at the node's control port, which provides a data output to a control terminal, e.g., the StrataView Plus Network Management Station.

Table 2-30 summarizes the alarm conditions and the resulting indications.

One set of alarm relays is used to signal a major alarm or minor alarm on the node. One pair of contacts on each relay is used for audible alarms. These contacts are in series with a faceplate alarm cut-off (ACO) switch. The other set of relay contacts are used for visual alarms and are not affected by the ACO switch. When the ACO switch is activated, a faceplate ACO indicator lights up as a reminder to the operator. If the ACO switch is activated to disable the node's audible alarm output and a second alarm occurs, the audible alarm is re-activated. Two faceplate LEDs provide local indication of network alarms.

**Table 2-30 ARM Card Set Alarm Reporting**

<b>Alarm Category</b>	<b>Alarm Severity</b>	<b>Faceplate Indication</b>	<b>Relay Indication</b>
Network	Major	none	Single form-C relay closed or open.
	Minor	none	Single form-C relay closed or open.
Node	Major	MAJOR LED (red)	Visual and audible relay closed only (normally open).
	Minor	MINOR LED (yellow)	Visual and audible form-C relay closed or open.
Alarm Cutoff	n.a.	ACO LED (green)	Interrupts audible relay closed.
Alarm History	n.a.	HIST LED (green)	None.

A second, independent, set of alarm outputs are provided to report network alarms. If a major or minor alarm occurs anywhere in the network—not necessarily on a particular node—these alarm relays are activated.

Major alarm relays are normally energized, providing an open circuit, so that a complete power failure (relay de-energized) at the node will result in a contact closure and resulting major alarm output. Minor alarms, however, are normally de-energized to conserve power and both contact closures as well as contact openings are available for minor alarms.

When an alarm condition clears, the alarm relays return to their normal state to clear the alarm outputs. A faceplate history indicator is turned on for each node alarm occurrence. The indicator will remain on even though an alarm may have already cleared. The history indicator is manually cleared by pressing a faceplate switch.

Two additional relays may operate under software control to report any other condition. These are not currently used, but future releases of system software may define a use.

The alarm reporting feature requires a card set that includes an ARM front card and an ARI back card. This card set can reside in any slot except the reserved slots. However, StrataCom recommends that slot 8 be used. Since a failure of either of these cards does not affect customer service, card redundancy is not necessary.

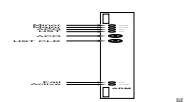
Alarm Relay Module (ARM)

The alarm relays are controlled by system software through Control Bus commands. The ARM interfaces with the Control Bus to receive alarm signals from the NPM and to send status signals back to the NPM. It does not connect to the CELLBUS because no customer data is packetized. The firmware on the ARM decodes the alarms.

The ARM faceplate contains the alarm LEDs, ACO and History Clear push buttons, and the active and fail LEDs indicating the status of the ARM card (see Figure 2-40 and Table 2-31). The ARM card is used in conjunction with an ARI card. The ARI card connects to the ARM at the P2 connector. Relay drive signals originate in the ARM to operate relays on the ARI.

The ARM periodically runs a background self-test to determine the state of the card. If the card fails this self-test, the faceplate FAIL LED turns on, and the ACTIVE LED turns off.

Figure 2-40 ARM Faceplate





**Table 2-31 ARM Faceplate Controls and Indicators**

Indicator	Function
MINOR LED (yellow)	Indicates a failure in the local node that is not service-affecting but still should be investigated. It could indicate problems such as a loss of redundancy, a low error rate on a digital trunk (frame bit errors or bipolar errors), etc.
MAJOR LED (red)	Indicates a failure in the local node that is service-affecting, such as an IGX 8 card failure, that should be attended to immediately.
ACO LED (white)	Indicates that a MINOR or MAJOR alarm is present, and that the alarm cutoff (ACO) button (5) was pressed to silence an accompanying audible alarm. The ACO light is turned off when the alarm condition is cleared.
HISTory light (green)	Indicates that an alarm has occurred sometime in the past on the node. The alarm may be current or it may have cleared. Pressing the HIST CLR button (6) turns off this light if there is no current alarm.
ACO button	When pressed, silences audible alarm (visual alarms remain on) and turns on ACO light (3).
HIST CLR button	When pressed, turns off HIST light (4) if there is no current alarm.
FAIL light (red)	Indicates that the card has failed self-test (an error occurred). Resetting the card with the <b>resetcd f</b> command is suggested first. If the LED comes on again, call the StrataCom ISC.
ACTIVE light (green)	Indicates that the card is active, has been assigned using the <b>addalmslot</b> command, and is functioning normally.

The installation of the ARM cards requires the removal of the node from service. The ARM can be physically installed in any front slot except slots normally reserved for the node processor cards (NPMs). The reserved slot numbers are 1 and 2. For standardization, StrataCom recommends that the ARM go in slot 8. The corresponding back slot must have an ARI card. The ARI card plugs directly in the ARM card.

## User Commands

Three commands are used with the ARM card set:

- **addalmslot**
- **delalmslot**
- **dspcd**

## Alarm Relay Interface Description (ARI)

The Alarm Relay Interface (ARI) card contains the alarm relays and their associated relay drivers. Alarm outputs are dry contact closures or opening contacts from Form C relays. The user must supply the voltage source to be switched by the IGX. Any source or load can be switched as long as it meets the following requirements.

- Voltage source, max. 220 Volts
- Steady-state current, max. 0.75 Amps
- Power dissipation, max. 60 Watts

A female DB37 connector resides on the faceplate for connection to the customer's office alarm or alarm-reporting system. Refer to Figure 2-41 for an illustration of the ARI faceplate.

**Figure 2-41     ARI Faceplate**



## Maintenance and Troubleshooting

The following paragraphs describe the maintenance and troubleshooting features associated with the ARM card set. Preventive maintenance is not necessary.

### Card Self Test

As with all IGX 8 cards, the ARM has diagnostic routines that periodically run to test the card's performance. These self-test diagnostics are run in the background, so they do not disrupt normal traffic. If a failure is detected during the self test, the faceplate red FAIL LED is turned on. In addition, the status of the card can be checked at the control terminal by using the Display Card (**dspcd**) command.

If a card failure is reported, the report remains until cleared. A card failure is cleared by using the Reset Card (**resetcd**) command. Two types of resets are available. They are hardware and failure. The reset failure clears the event log of any failure detected by the card self test and does not disrupt card operation. The hardware reset reboots the firmware and resets the card, and this momentarily disables the card.

### Card Replacement

Replacing an ARM card is done the same way as other back cards. For these procedures, refer to the repair and replacement description in the *IGX 8 Installation Guide*.

## Optional Peripherals

At least one node in a network has a StrataView Plus terminal, a control terminal, or a dial-in modem connected to it. Any control terminal connected in the network can configure, manage, monitor, and diagnose the entire network. In addition, at least one node in a network may have a printer connected to it for printing error and event reports.

The control terminal and printer connect to two RS-232 serial ports. These ports are the Control Terminal and Auxiliary Port on the SCM faceplate. These serial ports support all standard asynchronous data rates from 1200 to 19,200 baud. The default rate is 9600 baud. Data rates and the type of equipment connected to the ports are software-configurable.

StrataCom recommends that at least one IGX 8 in the network connects to a direct-dial modem so that StrataCom ISC personnel can perform remote diagnostic tests. A direct-dial modem connects to the backplane at the Control Terminal port. In addition, an auto-dial modem can be connected to the AUX port connector on the SCM at any node in the network so that remote error messages and alarms can be sent to the ISC, where they are automatically logged.

An external clock source can be connected to the SCM card using the external clock adapter cable. The external clock device can be either 1.544 MHz or 2.048 MHz RS-422 square wave signals depending on the primary application of the IGX 8 (T1 or E1). Selection is made through software. The clock stability should be at least as good as a STRATUM 3 clock source. Appendix B lists the terminals, printers, modems and clock sources that have been tested and approved for use with the IGX.

